

e-Front runners

FUJI POWER MOSFET

Super FAP-E³ series

N-CHANNEL SILICON POWER MOSFET

Features

Maintains both low power loss and low noise Lower R_{DS}(on) characteristic

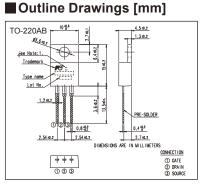
More controllable switching dv/dt by gate resistance Smaller V_{GS} ringing waveform during switching Narrow band of the gate threshold voltage (3.0±0.5V) High avalanche durability

Applications

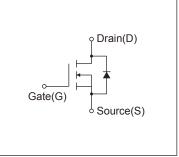
Switching regulators UPS (Uninterruptible Power Supply) DC-DC converters

Maximum Ratings and Characteristics

Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)



Equivalent circuit schematic



Description	Symbol	Characteristics	Unit	Remarks
Durin Onume Vieltene	VDS	500	V	
Drain-Source Voltage	VDSX	500	V	V _{GS} = -30V
Continuous Drain Current	lo	±12	A	
Pulsed Drain Current	IDP	±48	A	
Gate-Source Voltage	Vgs	±30	V	
Repetitive and Non-Repetitive Maximum Avalanche Current	lar	12	A	Note*1
Non-Repetitive Maximum Avalanche Energy	Eas	400	mJ	Note*2
Repetitive Maximum Avalanche Energy	Ear	16.5	mJ	Note*3
Peak Diode Recovery dV/dt	dV/dt	6.5	kV/µs	Note*4
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5
Maulinum Daura Dia dia stian	PD	2.02	14/	Ta=25°C
Maximum Power Dissipation		165	W	Tc=25°C
	Tch	150	°C	
Operating and Storage Temperature range	Tstg	-55 to +150	°C	
Isolation Voltage	VISO	2	kVrms	t = 60sec, f = 60Hz

• Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions		min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BVDSS	ID=250µA, VGS=0V		500	-	-	V
Gate Threshold Voltage	Vgs (th)	ID=250µA, VDS=VGS		2.5	3.0	3.5	V
Zero Gate Voltage Drain Current		V _{DS} =500V, V _{GS} =0V	Tch=25°C	-	-	25	μA
	IDSS	V _{DS} =400V, V _{GS} =0V	Tch=125°C	-	-	250	
Gate-Source Leakage Current	Igss	V _{GS} =±30V, V _{DS} =0V		-	10	100	nA
Drain-Source On-State Resistance	RDS (on)	ID=6A, VGS=10V		-	0.444	0.52	Ω
Forward Transconductance	g fs	ID=6A, VDS=25V		6.5	13	-	S
Input Capacitance	Ciss	V _{DS} =25V V _{GS} =0V f=1MHz		-	1600	2400	pF
Output Capacitance	Coss			-	160	240	
Reverse Transfer Capacitance	Crss			-	11.5	17.5	
Turn-On Time	td(on)	V _{cc} =300V V _{cs} =10V I _D =6A R _s =15Ω		-	20	30	ns
	tr			-	9	13.5	
Turn-Off Time	td(off)			-	100	150	
	tf			-	18	27	
Total Gate Charge	QG	V _{cc} =300V I _D =12A V _{cs} =10V		-	47	70.5	nC
Gate-Source Charge	QGS			-	10.5	16	
Gate-Drain Charge	QGD			-	14	21	
Avalanche Capability	lav	L=2.12mH, Tch=25°C		12	-	-	A
Diode Forward On-Voltage	Vsd	IF=12A, VGS=0V, Tch=25°C		-	0.88	1.32	V
Reverse Recovery Time	trr	I _F =12A, V _{GS} =0V		-	0.36	-	μs
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	4.1	-	μC

• Thermal Characteristics

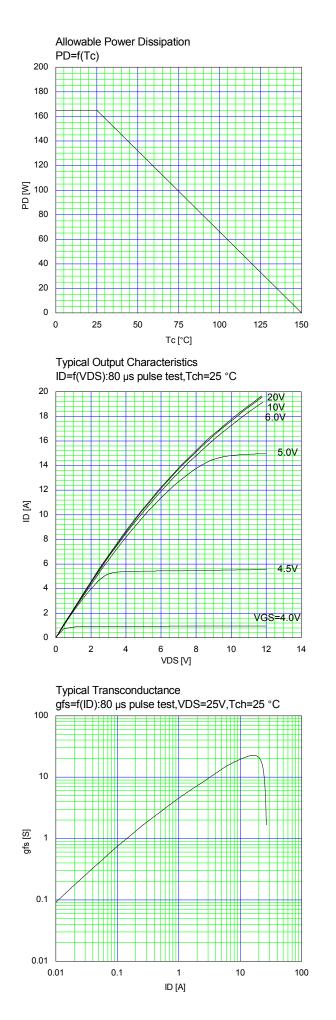
Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to Case			0.758	°C/W
	Rth (ch-a)	Channel to Ambient			62.0	°C/W

Note *1 : Tch≤150°C

Note *2 : Stating Tch=25°C, IAs=5A, L=29.2mH, Vcc=50V, Rc=50Ω EAs limited by maximum channel temperature and avalanche current. See to 'Avalanche Energy' graph. Note *3 : Repetitive rating : Pulse width limited by maximum channel temperature.

See to the 'Transient Themal impeadance' graph. Note *4 : I⊧≤-Ip, -di/dt=100A/µs, Vcc≤BVpss, Tch≤150°C.

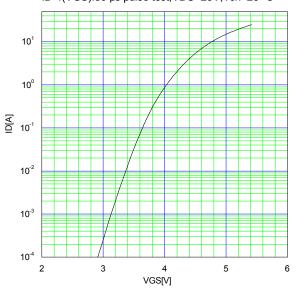
Note *5 : I⊧≤-ID, dv/dt=6.5kV/µs, Vcc≤BVDss, Tch≤150°C.



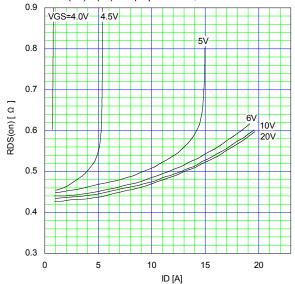
ID=f(VDS):Duty=0(Single pulse),Tc=25 °c 10² t= 1µs 10µs 10¹ 100 10⁰ ID [A] 1ms Power loss waveform Square waveform 10⁻¹ P_D DC 10⁻² 10⁰ 10¹ VDS [V] 10² 10⁻¹ 10³

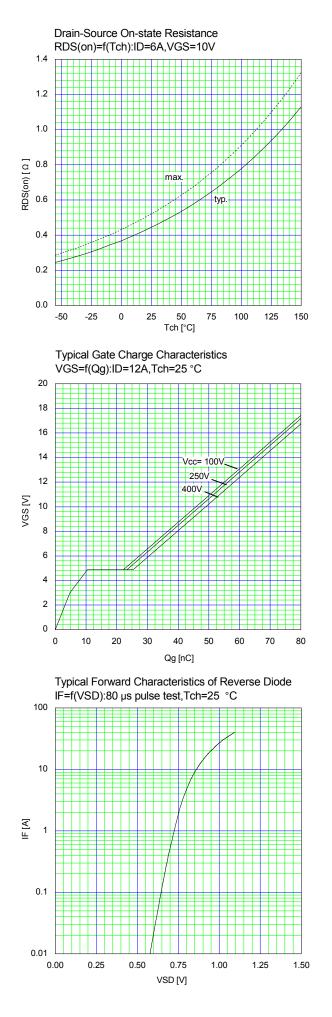
Safe Operating Area

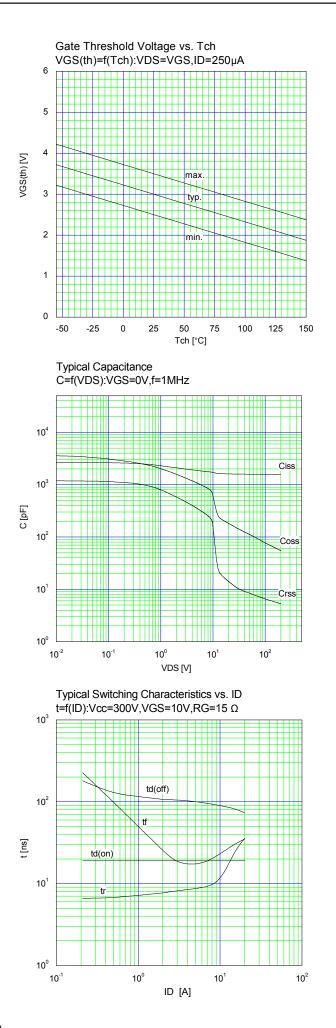
Typical Transfer Characteristic ID=f(VGS):80 µs pulse test,VDS=25V,Tch=25 °C

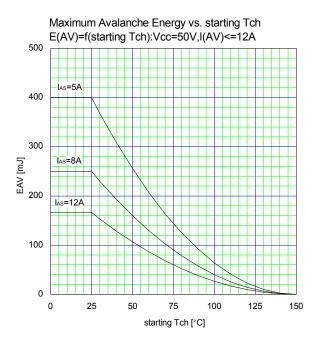


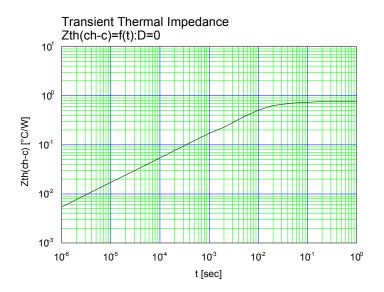
Typical Drain-Source on-state Resistance RDS(on)=f(ID):80 µs pulse test,Tch=25 °C











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