

FMV06N60ES

FUJI POWER MOSFET

Super FAP-E^{3S} series

N-CHANNEL SILICON POWER MOSFET

■ Features

Maintains both low power loss and low noise Lower $R_{DS}(on)$ characteristic More controllable switching dv/dt by gate resistance Smaller V_{GS} ringing waveform during switching Narrow band of the gate threshold voltage (3.7±0.5V) High avalanche durability

Applications

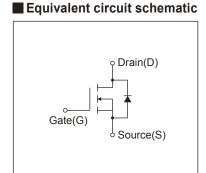
Switching regulators
UPS (Uninterruptible Power Supply)
DC-DC converters

■ Maximum Ratings and Characteristics

Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)

TO-220F (SLS) 10-8.5

■ Outline Drawings [mm]



Description Symbol Characteristics Unit Remarks V_{DS} **Drain-Source Voltage** V_{GS} = -30V VDSX 600 V **Continuous Drain Current** ΙD ±6 Α **Pulsed Drain Current** IDP ±24 Α Gate-Source Voltage Vgs ±30 Repetitive and Non-Repetitive Maximum Avalanche Current I_{AR} 6 Α Note*1 Non-Repetitive Maximum Avalanche Energy 313.7 Note*2 EAS mJ Repetitive Maximum Avalanche Energy E_{AR} 3.7 mJ Note*3 Peak Diode Recovery dV/dt dV/dt 3.8 Note*4 kV/us Peak Diode Recovery -di/dt -di/dt 100 Note*5 A/µs 2.16 Ta=25°C **Maximum Power Dissipation** P_{D} W 37 Tc=25°C Tch 150 °C **Operating and Storage Temperature range** Tstg -55 to +150 °C Isolation Voltage kVrms t = 60 sec, f = 60 HzViso 2

● Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions		min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BVDSS	I _D =250μA, V _{GS} =0V		600	-	-	V
Gate Threshold Voltage	V _{GS} (th)	I _D =250µA, V _{DS} =V _{GS}		3.2	3.7	4.2	V
Zero Gate Voltage Drain Current		V _{DS} =600V, V _{GS} =0V	T _{ch} =25°C	-	-	25	μА
	IDSS	V _{DS} =480V, V _{GS} =0V	T _{ch} =125°C	-	-	250	
Gate-Source Leakage Current	Igss	V _{GS} =±30V, V _{DS} =0V		-	10	100	nA
Drain-Source On-State Resistance	R _{DS} (on)	I _D =3A, V _{GS} =10V		-	1.03	1.20	Ω
Forward Transconductance	g _{fs}	I _D =3.0A, V _{DS} =25V		2.5	5	-	S
Input Capacitance	Ciss	V _{DS} =25V	-	950	1425	pF	
Output Capacitance	Coss	V _{GS} =0V	-	100	150		
Reverse Transfer Capacitance	Crss	f=1MHz	-	7.5	11		
Turn-On Time	td(on)	Vcc=300V	-	29	43.5	ns	
	tr	V _{GS} =10V I _D =3.0A		-	15		22.5
Turn-Off Time	td(off)			-	75		113
	tf	R _G =27Ω	-	16	24		
Total Gate Charge	Q _G			-	31	46.5	nC
Gate-Source Charge	Qss	V₀=300V I₀=6A	-	10.5	15.8		
Gate-Drain Charge	Q _{GD}	- ID=6A - V _{GS} =10V		-	8	12	
Gate-Drain Crossover Charge	Qsw	V63-10 V	-	4.5	6.75		
Avalanche Capability	lav	L=6.39mH, Tch=25°C		6	-	-	Α
Diode Forward On-Voltage	V _{SD}	I _F =6A, V _{GS} =0V, T _{ch} =25°C		-	0.90	1.35	V
Reverse Recovery Time	trr	I _F =6A, V _{GS} =0V		-	0.4	-	μS
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	3.3	-	μC

Thermal Characteristics

Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to case			3.38	°C/W
	Rth (ch-a)	Channel to ambient			58.0	°C/W

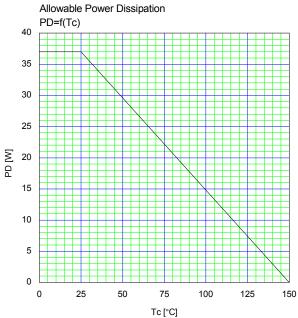
Note *1 : Tch≤150°C

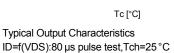
Note *2 : Stating Tch=25°C, Ias=2.4A, L=99.8mH, Vcc=60V, R_G=50Ω
Eas limited by maximum channel temperature and avalanche current.
See to 'Avalanche Energy' graph.

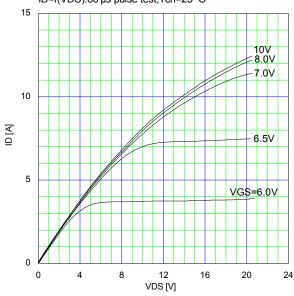
Note *3 : Repetitive rating : Pulse width limited by maximum channel temperature.

See to the 'Transient Themal impeadance' graph.

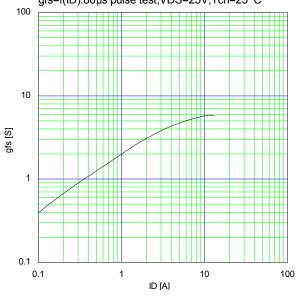
Note *4 : IF≤-ID, -di/dt=100A/µs, Vcc≤BVDss, Tch≤150°C. Note *5 : IF≤-ID, dv/dt=3.8kV/µs, Vcc≤BVDss, Tch≤150°C FMV06N60ES FUJI POWER MOSFET



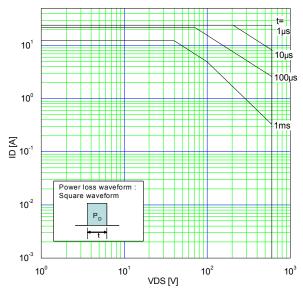




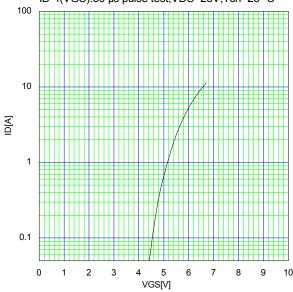
Typical Transconductance gfs=f(ID):80µs pulse test,VDS=25V,Tch=25 °C



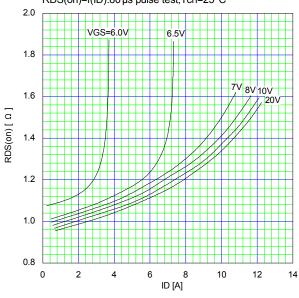
Safe Operating Area I_D=f(V_{DS}):Duty=0(Single pulse),Tc=25 °c



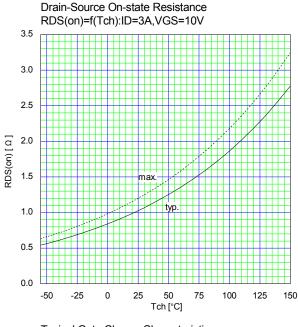
Typical Transfer Characteristic ID=f(VGS):80 µs pulse test,VDS=25V,Tch=25 °C

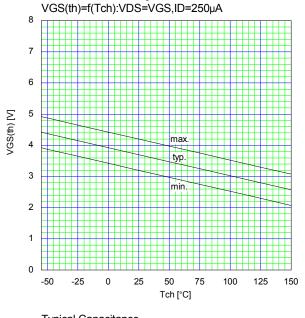


Typical Drain-Source on-state Resistance RDS(on)=f(ID):80 µs pulse test,Tch=25°C

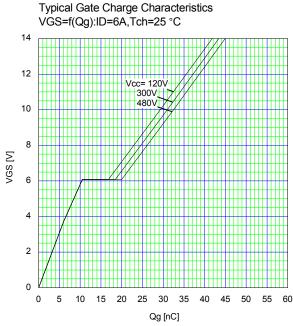


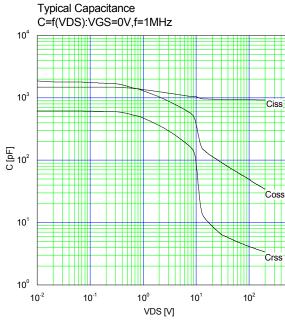
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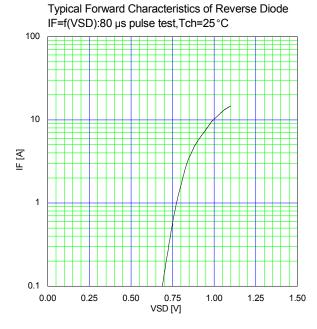


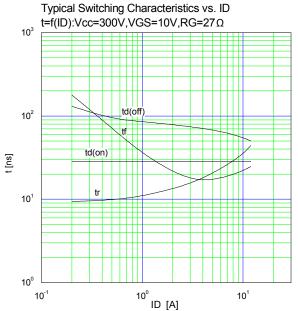


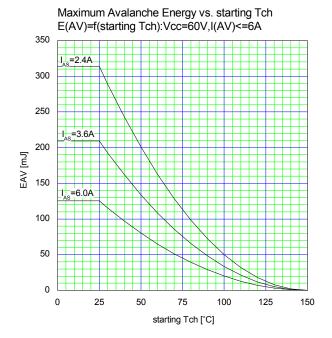
Gate Threshold Voltage vs. Tch

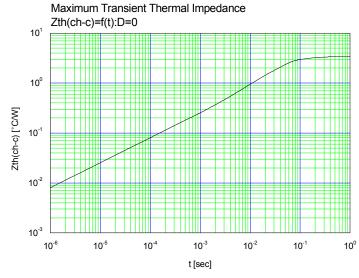












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