# F Fuji Electric FMV06N90E

## Innovating Energy Technology

http://www.fujielectric.com/products/semiconductor/ **FUJI POWER MOSFET** 

# Super FAP-E<sup>3</sup> series

### N-CHANNEL SILICON POWER MOSFET

#### Features

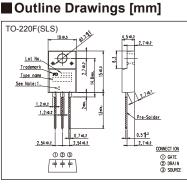
Maintains both low power loss and low noise Lower R<sub>DS</sub>(on) characteristic More controllable switching dv/dt by gate resistance Smaller VGs ringing waveform during switching Narrow band of the gate threshold voltage (4.0±0.5V) High avalanche durability

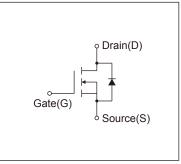
#### Applications

Switching regulators UPS (Uninterruptible Power Supply) **DC-DC converters** 

#### Maximum Ratings and Characteristics

#### Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)





Equivalent circuit schematic

Description	Symbol	Characteristics	Unit	Remarks
Drain Source Veltere	VDS	900	V	
Drain-Source Voltage	VDSX	900	V	V <sub>GS</sub> = -30V
Continuous Drain Current	lo	±6	A	
Pulsed Drain Current	IDP	±24	А	
Gate-Source Voltage	Vgs	±30	V	
Repetitive and Non-Repetitive Maximum AvalancheCurrent	lar	6	А	Note*1
Non-Repetitive Maximum Avalanche Energy	Eas	323.6	mJ	Note*2
Repetitive Maximum Avalanche Energy	Ear	4.8	mJ	Note*3
Peak Diode Recovery dV/dt	dV/dt	2.0	kV/μs	Note*4
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5
Manimum Danna Dia sin stian	P₀	2.16	10/	Ta=25°C
Maximum Power Dissipation		48	W	Tc=25°C
On another and Otenson Tenson sectors are as	Tch	150	°C	
Operating and Storage Temperature range	Tstg	-55 to + 150	°C	
Isolation	Viso	2	KVrms	t=60sec, f=60Hz

#### Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions		min.	typ.	max.	Unit	
Drain-Source Breakdown Voltage	BVDSS	ID=250µA, VGS=0V		900	-	-	V	
Gate Threshold Voltage	V <sub>GS</sub> (th)	ID=250µA, VDS=VGS	ID=250µA, VDS=VGS		4.0	4.5	V	
		V <sub>DS</sub> =900V, V <sub>GS</sub> =0V	Tch=25°C	-	-	25		
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =720V, V <sub>GS</sub> =0V	Tch=125°C	-	-	250	μΑ	
Gate-Source Leakage Current	Igss	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V		-	10	100	nA	
Drain-Source On-State Resistance	RDS (on)	ID=3A, VGS=10V		-	2.1	2.5	Ω	
Forward Transconductance	g <sub>fs</sub>	ID=3.0A, VDS=25V		3.5	7.0	-	S	
Input Capacitance	Ciss	V <sub>DS</sub> =25V		-	980	1500		
Output Capacitance	Coss			-	95	150	pF	
Reverse Transfer Capacitance	Crss			6.5	10	1		
tr	td(on)	Vcc=600V Vcs=10V Ic=3.0A		-	33	50	ns	
	tr			-	32	48		
	td(off)			-	100	150		
Turn-Off Time	tf	Rg=39Ω		-	32	48	1	
Total Gate Charge	QG	Vcc=450V Ic=6A Vcs=10V		-	33	50	nC	
Gate-Source Charge	QGS			-	10	15		
Drain-Source Crossover Charge	Qsw			-	3.5	5.5		
Gate-Drain Charge	QGD			-	11	17		
Avalanche Capability	lav	L=6.59mH, Tch=25°C		6	-	-	A	
Diode Forward On-Voltage	Vsd	IF=6A, VGS=0V, Tch=25°C		-	0.90	1.35	V	
Reverse Recovery Time	trr	IF=6A, VGS=0V		-	1.6	-	μS	
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	9.5	-	μC	

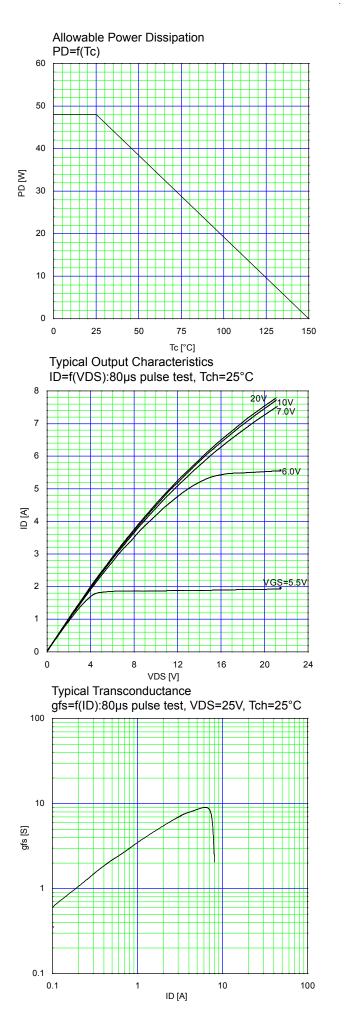
#### Thermal Characteristics

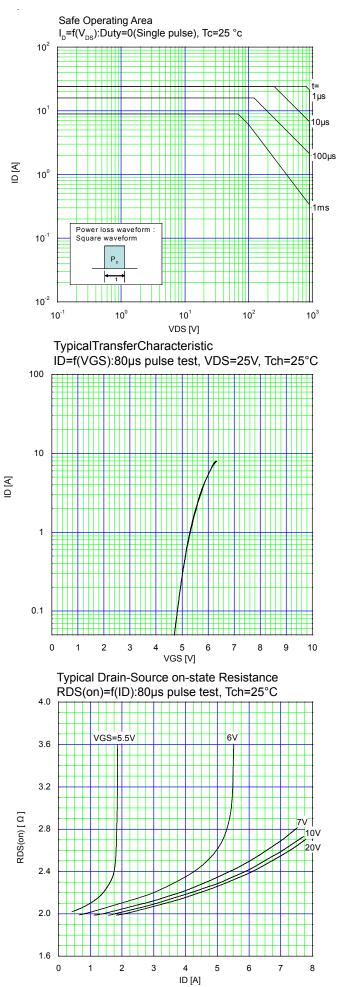
Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermel registeres	Rth (ch-c)	Channel to case			2.6	°C/W
Thermal resistance	Rth (ch-a)	Channel to ambient			58.0	°C/W

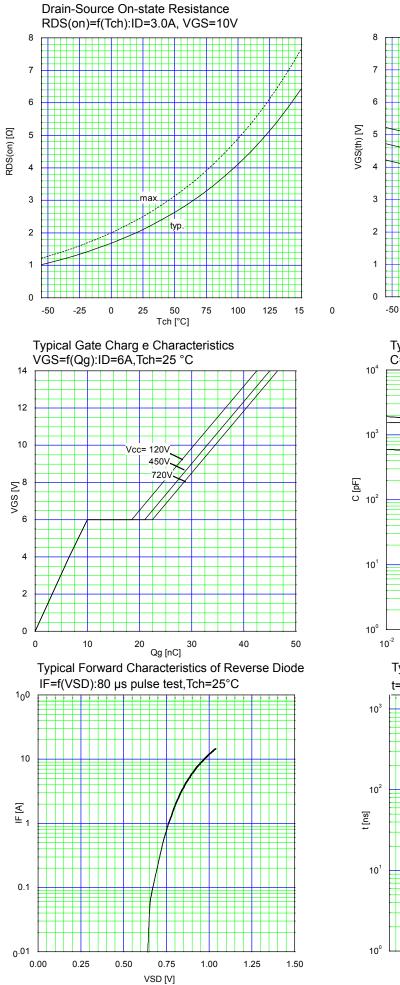
Note \*1 : Tch≤150°C.

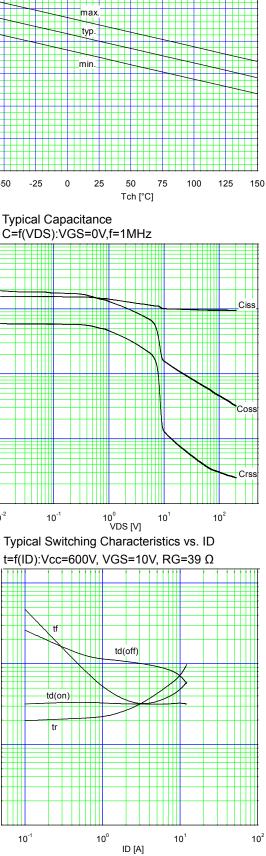
Note \*2 : Stating Tch=25°C, IAs=2.4A, L=103mH, Vcc=90V, Rg=10Ω, Eas limited by maximum channel temperature and avalanche current. Note \*3 : Repetitive rating : Pulse width limited by maximum channel temperature. Note \*4 : I⊧≤-I₀, -di/dt=100A/μ₅, Vcc≤BV₀ss, Tch≤150°C.

Note \*5 : IFS-ID, dv/dt=2.0kV/µs, VccSBVDss, TchS150°C



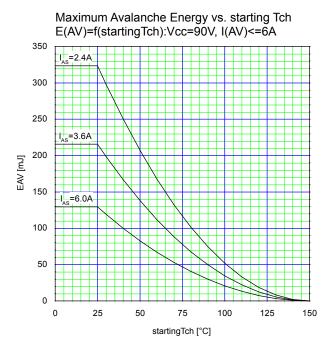


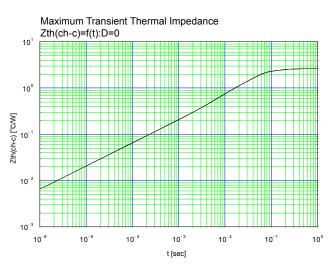




Gate Threshold Voltage vs. Tch

VGS(th)=f(Tch):VDS=VGS, ID=250µA





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