

FMV07N50E

FUJI POWER MOSFET

Super FAP-E³ series

N-CHANNEL SILICON POWER MOSFET

■ Features

Maintains both low power loss and low noise Lower R_{DS}(on) characteristic More controllable switching dv/dt by gate resistance Smaller V_{GS} ringing waveform during switching Narrow band of the gate threshold voltage (3.0±0.5V) High avalanche durability

Applications

Switching regulators UPS (Uninterruptible Power Supply) DC-DC converters

Maximum Ratings and Characteristics

● Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)

TO-220F(SLS) 2,7±0.2 ① GATE ② DRAIN ③ SOURCE

■ Outline Drawings [mm]

Drain(D)

Gate(G)

■ Equivalent circuit schematic

Description	Symbol	Characteristics	Unit	Remarks	
Proin Source Voltage	V _{DS}	500	V		
Drain-Source Voltage	V _{DSX}	500	V	V _{GS} = -30V	
Continuous Drain Current	I _D	±6.5	Α		
Pulsed Drain Current	IDP	±26	Α		
Gate-Source Voltage	V _{GS}	±30	V		
Repetitive and Non-Repetitive Maximum Avalanche Current	Iar	6.5	Α	Note*1	
Non-Repetitive Maximum Avalanche Energy	Eas	266	mJ	Note*2	
Repetitive Maximum Avalanche Energy	Ear	3.2	mJ	Note*3	
Peak Diode Recovery dV/dt	dV/dt	5.4	kV/µs	Note*4	
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5	
Martin and Programme Control		2.16	144	Ta=25°C	
Maximum Power Dissipation	P□	32	W	Tc=25°C	
O	Tch	150	°C		
Operating and Storage Temperature range	Tstg	-55 to +150	°C		
Isolation Voltage	Viso	2	kVrms	t = 60sec, f = 60H	

● Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions	Conditions		typ.	max.	Unit
Drain-Source Breakdown Voltage	BVDSS	I _D =250μA, V _{GS} =0V		500	-	-	V
Gate Threshold Voltage	V _{GS} (th)	I _D =250μA, V _{DS} =V _{GS}		2.5	3.0	3.5	V
Zero Gate Voltage Drain Current		V _{DS} =500V, V _{GS} =0V	T _{ch} =25°C	-	-	25	μΑ
	IDSS	V _{DS} =400V, V _{GS} =0V	T _{ch} =125°C	-	-	250	
Gate-Source Leakage Current	Igss	V _{GS} =±30V, V _{DS} =0V		-	10	100	nA
Drain-Source On-State Resistance	R _{DS} (on)	I _D =3.3A, V _{GS} =10V		-	0.73	0.85	Ω
Forward Transconductance	g fs	I _D =3.3A, V _{DS} =25V		3.5	7	-	S
Input Capacitance	Ciss	V _{DS} =25V		-	1050	1575	
Output Capacitance	Coss	V _{GS} =0V f=1MHz		-	95	142.5	pF
Reverse Transfer Capacitance	Crss			-	7	10.5	
Turn-On Time	td(on)	V _{cc} =300V V _{GS} =10V I _D =3.3A R _G =10Ω		-	11	16.5	ns
	tr			-	7	10.5	
Turn-Off Time	td(off)			-	75	113	
	tf			-	14	21	
Total Gate Charge	Q _G	V _{cc} =250V I _D =6.5A V _{os} =10V		-	32	48	nC
Gate-Source Charge	QGS			-	8	12	
Gate-Drain Charge	Q _{GD}			-	9	13.5	
Avalanche Capability	lav	L=4.61mH, Tch=25°C		6.5	-	-	Α
Diode Forward On-Voltage	V _{SD}	I _F =6.5A, V _{GS} =0V, T _{ch} =25°C		-	0.86	1.30	V
Reverse Recovery Time	trr	I _F =6.5A, V _{GS} =0V		-	0.34	-	μs
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	3.0	-	μC

Thermal Characteristics

Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to Case			3.910	°C/W
	Rth (ch-a)	Channel to Ambient			58.0	°C/W

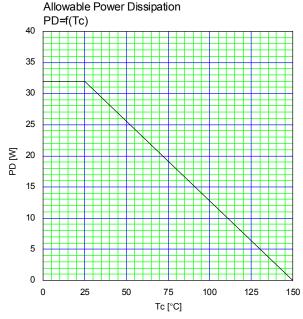
Note *1 : Tch≤150°C

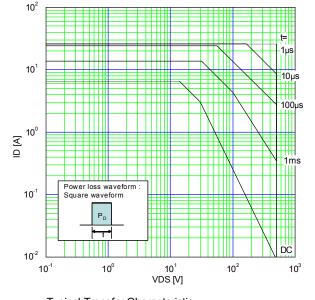
Note *2 : Stating Tch=25°C, Ias=2.6A, L=72.1mH, Vcc=50V, Rg=50 Ω Eas limited by maximum channel temperature and avalanche current. See to 'Avalanche Energy' graph.

Note *3 : Repetitive rating : Pulse width limited by maximum channel temperature

See to the 'Transient Themal impeadance' graph.

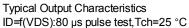
Note *4 : IFS-ID, -di/dt=100A/µs, Vcc≤BVoss, Tch≤150°C. Note *5 : IF \leq -ID, dv/dt = 5.4kV/ μ s, Vcc \leq BVDSS, Tch \leq 150°C.

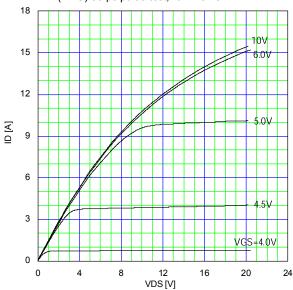




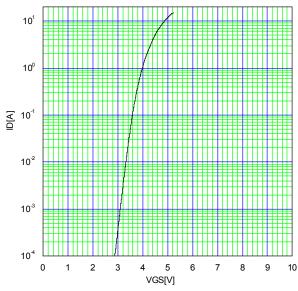
Safe Operating Area

ID=f(VDS):Duty=0(Single pulse),Tc=25 °c

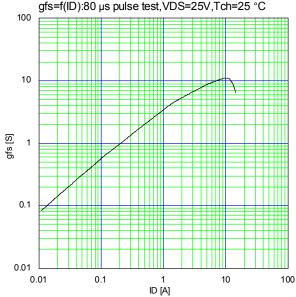




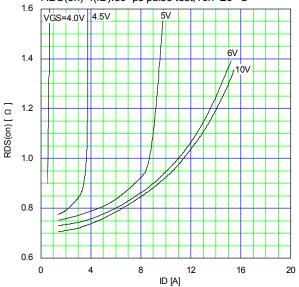
Typical Transfer Characteristic ID=f(VGS):80 µs pulse test,VDS=25V,Tch=25 °C

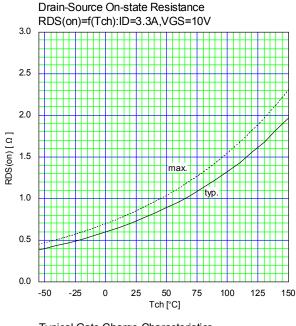


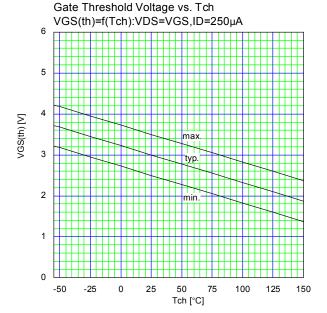
Typical Transconductance gfs=f(ID):80 µs pulse test, VDS=25V,Tch=25 °C

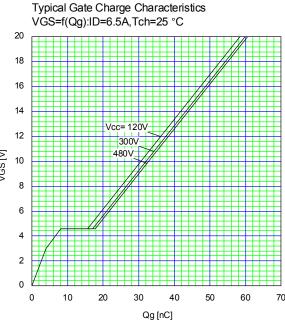


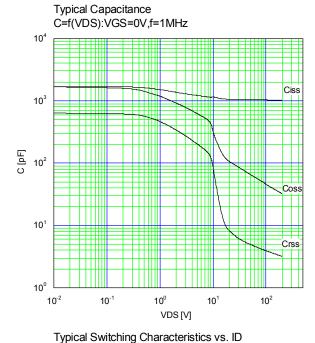
Typical Drain-Source on-state Resistance RDS(on)=f(ID):80 µs pulse test,Tch=25 °C

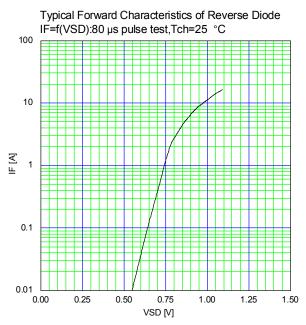


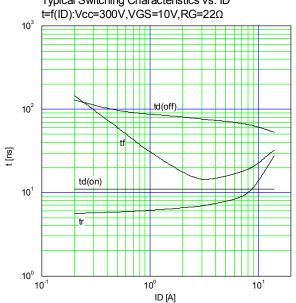


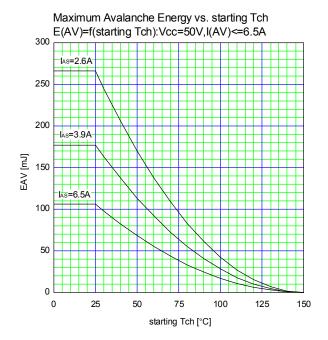


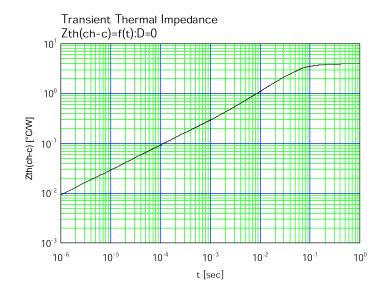












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