# **UTC** UNISONIC TECHNOLOGIES CO., LTD

# UTT100N05

Preliminary

# 100A, 50V N-CHANNEL POWER MOSFET

## DESCRIPTION

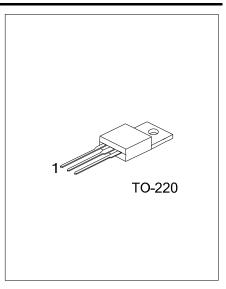
The UTC **UTT100N05** is an N-channel enhancement mode power MOSFET using UTC's advanced technology to provide customers with minimum on-state resistance and superior switching performance.

## FEATURES

- \* R\_{DS(ON)}= 7m\Omega @ V\_{GS}=10V, I\_D=50A
- $R_{DS(ON)}$ = 10m $\Omega$  @ V<sub>GS</sub>=4.5V, I<sub>D</sub>=50A
- \* High switching speed
- \* Improved dv/dt capability

## ORDERING INFORMATION

Ordering Number		Deekege	Pin Assignment			Dealing	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UTT100N05L-TA3-T	UTT100N05L-TA3-T UTT100N05G-TA3-T		G	D	S	Tube	
Note: Pin Assignment: G: Gate D: Drain S: Source							
Note: Pin Assignment: G: Gate D: Drain S: Source		(1) T: Tube (2) TA3: TO-2 (3) G: Haloge		Lead Free	9		



#### ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V <sub>DSS</sub>	50	V
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
Drain Current	Continuous	Ι <sub>D</sub>	100	А
	Pulsed	I <sub>DM</sub>	400	Α
Avalanche Energy	Single Pulsed	E <sub>AS</sub>	875	mJ
Power Dissipation		PD	83	W
Junction Temperature		TJ	+150	°C
Storage Temperature		T <sub>STG</sub>	-55 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ <sub>JA</sub>	62.5	°C/W	
Junction to Case	θ <sub>JC</sub>	1.5	°C/W	

#### ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						UNIT
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250µA, V <sub>GS</sub> =0V	50			V
Drain-Source Leakage Current	IDSS	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V	00		10	μA
	orward	V <sub>GS</sub> =+20V, V <sub>DS</sub> =0V			+100	nA
Gate-Source Leakage Current Reverse		V <sub>GS</sub> =-20V, V <sub>DS</sub> =0V			-100	nA
					100	10.4
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA	1		3	V
×		V <sub>GS</sub> =10V, I <sub>D</sub> =50A		7		mΩ
Static Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =50A		10		mΩ
DYNAMIC PARAMETERS						
Input Capacitance	CISS			12900		рF
Output Capacitance	Coss	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1.0MHz		1060		рF
Reverse Transfer Capacitance	C <sub>RSS</sub>			700		рF
SWITCHING PARAMETERS						
Total Gate Charge	$Q_G$			500		nC
Gate to Source Charge	$Q_{GS}$	V <sub>DD</sub> =50V, I <sub>D</sub> =100A, V <sub>GS</sub> =10V		50		nC
Gate to Drain Charge	$Q_{GD}$			33		nC
Turn-ON Delay Time	t <sub>D(ON)</sub>			90		ns
Rise Time	t <sub>R</sub>	V <sub>DD</sub> =30V, I <sub>D</sub> =50A,		130	200	ns
Turn-OFF Delay Time	t <sub>D(OFF)</sub>	R <sub>G</sub> =0.4Ω, V <sub>GS</sub> =10V		768		ns
Fall-Time	t <sub>F</sub>			280	420	ns
SOURCE- DRAIN DIODE RATINGS AND	CHARACT	ERISTICS				
Maximum Body-Diode Continuous Current	Is		100			А
Maximum Body-Diode Pulsed Current	I <sub>SM</sub>		400			А
Drain-Source Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =100A, V <sub>GS</sub> =0V		1.0	1.5	V



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