8A,5A,3A LOW DROPOUT POSITIVE REGULATORS ADJUSTABLE AND FIXED

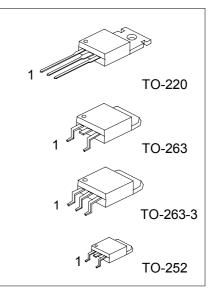
DESCRIPTION

The UTC U584/585/587 voltage regulators are monolithic integrated circuits, designed for use in applications requiring a well regulated positive output voltage with +5V input. The output voltage can be adjustable from 3.8 V down to 1.3V.

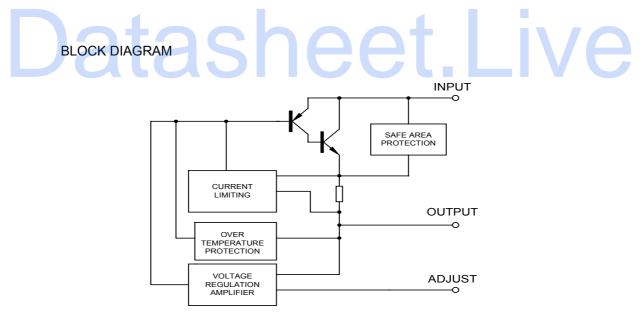
FEATURES

*Low dropout performance.

- *Adjustable output down to 1.3V.
- *Line regulation typically below 0.1%.
- *Load regulation typically below 0.1%.
- *Output current can be up to 8 A for UTC U584.
- *Three-terminal adjustable or fixed 3.3V.



1: ADJ/GND 2: OUTPUT 3: INPUT



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ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNIT
Input Voltage	VIN	7	V
Power Dissipation	PD	Internally Limited	W
Operating Junction	TJ	0 to 125	°C
Temperature Range			
Storage Temperature	Tstg	-65 to 150	°C
Lead Temperature	TLEAD	300	°C
(Soldering 10 Sec.)			

ELECTRICAL CHARACTERISTICS

PARAMETER	TEST CONDITIONS	Γ	MIN	TYP	MAX	UNIT
Reference Voltage		*	1.225	1.25	1.275	V
U584	1.3V≤(VIN-VOUT) ≤3V, 10mA≤IOUT≤8A		_	_	_	
U585	$1.3V \le (VIN-VOUT) \le 3V$, $10mA \le IOUT \le 5A$,					
U587	$1.3V \le (VIN-VOUT) \le 3V, 10mA \le IOUT \le 3A$					
Output Voltage		Ì	3.234	3.300	3.366	V
U584-3.3	4.75V≤V _{IN} ≤6.5V, 10mA≤IO∪T≤8A					
U585-3.3	4.75V≤V _{IN} ≤7V, 10mA≤IouT≤5A					
U587-3.3	4.75V≤V _{IN} ≤7V, 10mA≤IouT≤3A					
Line Regulation (Note 1.2)				0.1	0.2	%
U584/5/7	2.75V≤VIN≤7V, IOUT= 10mA					
U584/5/7-3.3	4.75V≤ VIN≤7V, IOUT= 0mA					
Load Regulation (Note 1, 2, 3)		*		0.2	1.0	%
U584/5/7	VIN-VOUT=2.5V.Tj=25°C, 10mA<=IOUT<=IFULLLOAD	1				
U584/5/7-3.3	V _{IN} =5V, Tj=25°C, 0mA≤I _{OUT} ≤ IFULLLOAD					
Dropout Voltage	$\Delta V_{\text{REF}}=1\%$, IOUT= IFULLLOAD	1				
1	Ti>=25°C			1.2	1.3	V
	Tj<=25°C			1.2	1.35	V
Current Limit (Note 3)	1]==20.0	*				А
U584	VIN-VOUT =3 V		8.0	8.5		~
U585	VIN-VOUT = 3 V		5.0	5.5		
U587	VIN-VOUT =3 V		3.0	3.6		
Adjust Pin Current				55	120	μA
Adjust Pin Current Change	1.5V<=(VIN-VOUT) <=3 V,	1		0.2	5	mA
(Note 3)	10mA<=IOUT<=IFULLLOAD			-	_	
Minimum Load Current	1.5V<=(VIN-VOUT) <=3V	*		2	10	mΑ
Quiescent Circuit Current	VIN<=5V	*		8	13	mΑ
Ripple Rejection	f=120Hz, Cout=25µATant,		60	72		dB
	VIN-VOUT =2.5V , IOUT= IFULLLOAD					
Temperature Stability				0.5		%
Long-Term Stability	TA=25°C, 1000Hrs			0.03	1.0	%
RMS Output Noise	TA=125°C, 10Hz<=f<=10kHz			0.03		%
(% of Vout)	,					
Thermal Resistance Junction to						°C /W
Case U584		1			1.6	
U587					3.0	
Thermal Resistance Junction to					60	°C /W
A_{MBIENT}, θ_{JA} U585						
Thermal Resistance Junction to		1			2.7	°C /W
Τ _{AB} , θ _{JT} U585						

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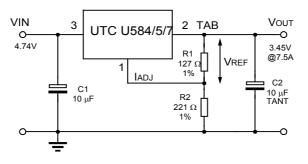
The * denotes specifications which apply over the specified operating temperature range.

Note 1: Load and line regulation are measured at a constant junction temperature by low duty cycle pulse testing. Note 2: Line and load regulation are guaranteed up to the maximum power dissipation (15W for the UTC U584,

10W for the UTC U585). Power dissipation is determined by input / output differential and the output current. Guaranteed maximum output power will not be available over the full input-output voltage range.

Note 3: IFULLLOAD is defined as the maximum value of output load current as a function of input-to-output voltage. Output current can be different for different input-to-output voltage.

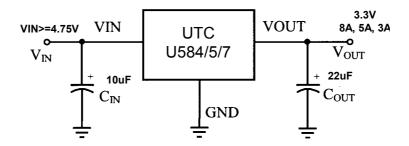
APPLICATION CIRCUIT (ADJUSTABLE)



REQUIRED FOR STABILITY

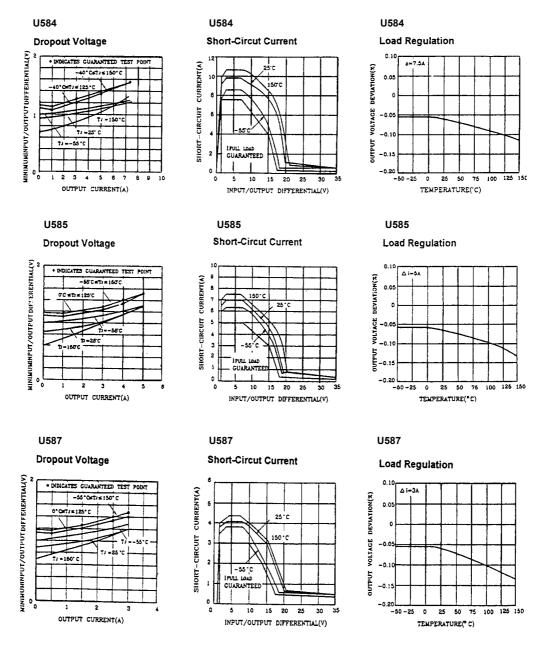
VOUT=VREF*(1+R2/R1)+ADJ*R2

APPLICATION CIRCUIT (3.3V)



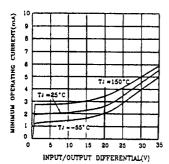
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TYPICAL PERFORMANCE CHARACTERISTICS



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Minimum Operating Current



Temperature Stability

1.2

1.2

1.2

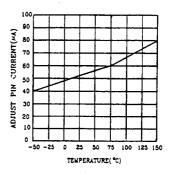
1.23

-50 -25 ٥ 25 50 76

VOI.TACE(V) 1.2

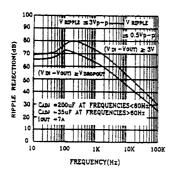
REFERENCE

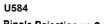
Adjust Pin Current







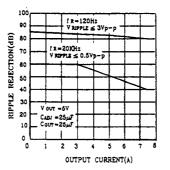




Ripple Rejection vs Current

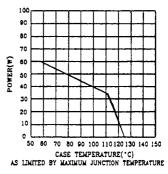
TENPERATURE(*C)

100 125 150



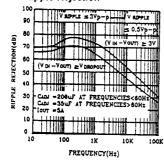


U584



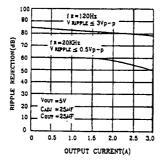


Ripple Rejection



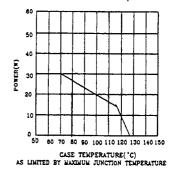


Ripple Rejection vs Current

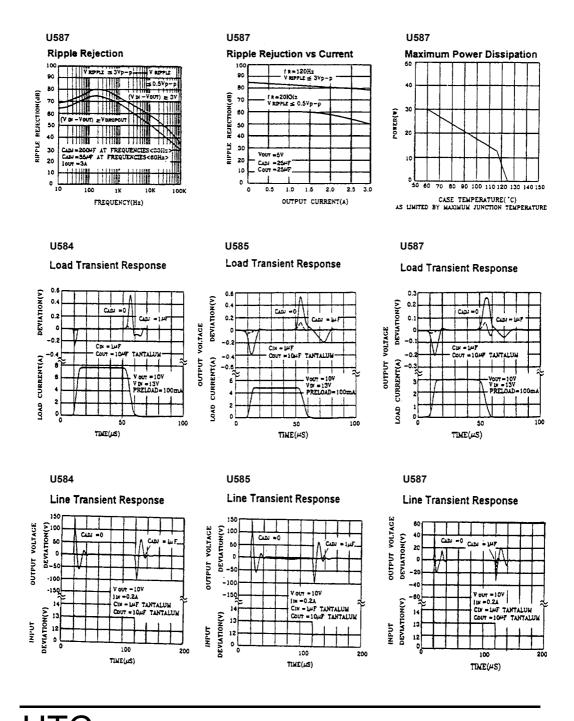


U585

Maximum Power Dissipation



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