Web page analyser for protocols on ENC28J60 based AVR microcontroller

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Abstract

Because the Internet is becoming more widespread, there was the possibility of communication with automation systems for configuration, reprogramming or control in real time. For this, the automation system needs a physical communication port with the network. This paper presents the design and implementation of such Ethernet interface for automation system with microcontroller, mainly analysing web page by making use of Protocols on AVR microcontroller and it focuses on the adoption of Agent Technology to analyse and evaluate the Web Content Traditional Web traffic analysis metrics such as page views, unique browser, visitor loyalty, etc have been used to analyse the Web traffic behaviour for a long time since the birth of World Wide Web, but the emersion of software robots and Web crawlers trigger a huge impact on the integrity and correctness of these traditional Web statistics. The connection between the automation system and the interface is made via an SPI (Serial Peripheral Interface) port. The main element of the interface is a chip named ENC28J60 manufactured by Microchip. It is designed to serve as an Ethernet network interface for any controller equipped with SPI. ENC28J60 meets all specifications of IEEE 802.3 having incorporated MAC and PHY modules. It also provides an internal mode DMA for fast data transfer and hardware for TCP/IP. With ENC28J60, an RJ45 connector and a few passive components we can connect a microcontroller to a 10 Mbps network. The access of ENC28J60 controller is done through configuration registers and memory. The interpretation of data to extract useful information is done by software routines. These interfaces have multiple applications: Remote monitoring of a system or industrial process, can be accessed on mobile devices with Internet connection, domestic applications.

1. Introduction

The paper aims to show how to make Ethernet interface with microcontroller automation systems. Are given at all stages of design, implementation and programming interface. The only requirement is that

the microcontroller to have SPI interface (Serial Peripheral Interface). SPI is a standard synchronous

high speed interface that operates in full duplex and can operate with one master device and one or more slave devices A big disadvantage of this port is that transmission can only be a short distance till to 50-100 centimetres.

2. The Design of Interface

2.1. The network controller

The main element is the controller interface ENC28J60 made by Microchip company. It is designed to serve as an Ethernet network interface for any controller equipped with SPI. ENC28J60 meets all specifications of IEEE 802.3 having incorporated MAC and PHY modules. It also provides an internal mode DMA for fast data transfer and hardware for TCP/IP. The Enc28J60 has a clock at 25MHz.



Figure 1: The ENC28J60

With Orcad 9.2 software package was designed electronic interface scheme, which is shown below. The PCB is shown in the figure achieved being conducted on two sides.

The LEDA and LEDB outputs of the controller can be used to view his activity. Their configuration is done

through a dedicated register. In this design these were connected to RJ45 network jack which will be described later. Next picture shows the footprints of components and the placement of theses on pcb.



Figure 2: ENC28J60 block diagram

2.2. The Magjack connector

To connect the circuit to a computer network requires a circuit consisting of coils, diodes and resistors to ensure the electrical parameters of the line, controller protection and the possibility of using POE (Power-Over-Ethernet). For this, it has been identified the connector presented below. It has integrated magnetic system and can use Power over Ethernet technology, providing speeds of 10/100Mbps. Connector has built two green LEDs that can be used to display various information ENC28J60 controller (using the register PHLCON: PHY control register LED modules)

3. Ethernet Interface

In the Ethernet interface module, Ethernet controller are divided into two layers according to its functions and it is shown in Fig. 4. One is media access controller (MAC) layer and the other is network physical (PHY) layer. They correspond to Layer 2 and Layer 1 in ISO model. MAC layer provides the treatment on data sending and receiving. It also provides an interface to PHY through an internal medium independent interface

3.1 limitations of Ethernet

There are practical limits to the size of our Ethernet network. A primary concern is the length of the shared cable. Electrical signals propagate along a cable very quickly, but they weaken as they travel and electrical interference from neighbouring devices (fluorescent lights, for example) can scramble the signal. A network cable must be short enough that devices at opposite ends can receive each other's signals clearly and with minimal delay. This places a distance limitation on the maximum separation between two devices on an Ethernet network. Additionally, since in CSMA/CD only a single device can transmit at a given time, there are practical limits to the number of devices that can coexist in a single network. Ethernet networks face congestion problems as they increased in size. If a large number of stations connected to the same segment and each generated a sizable amount of traffic, many stations may attempt to transmit whenever there was an opportunity. Under these circumstances, collisions would become more frequent and could begin to choke out successful transmissions, which could take inordinately large amounts of time to complete. One way to reduce congestion would be to split a single segment into multiple segments, thus creating multiple collision domains. This solution creates a different problem, as now these now separate segments are not able to share information with each other.

To alleviate these problems, Ethernet networks implemented bridges. Bridges connect two or more network segments, increasing the network diameter as a repeater does, but bridges also help regulate traffic. They can send and receive transmissions just like any other node, but they do not function the same as a normal node. The bridge does not originate any traffic of its own; like a repeater, it only echoes what it hears from other stations. Ethernet is versatile enough to suit many purposes. Probably the best known use for Ethernet is in linking desktop computers in offices, but that's not its only use. Ethernet can transfer any kind of data, from short messages to huge files. An Ethernet communication can take advantage of existing higherlevel protocols such as TCP and IP, or it can use an application-specific protocol. Ethernet doesn't require a large or fast computer. With the addition of an Ethernet

controller chip, even an 8-bit microcontroller can communicate in an Ethernet network.

4. The Automation System

Automation system was developed using a Atmel AVR microcontroller family: ATMEGA8. In fact, he was made a minimal automation system to allow testing.



Figure 3: The automation system



Figure 4: The electronic schematics

In the interconnection circuit is no conversion from 3.3V to 5V because the pins of ENC28J60 is a 5V voltage tolerant as the description from datasheet. After interconnection, the structure looks like following figure.

Atmega8	Ethernet interface
INT0	/INT
MISO	MISO
MOSI	MOSI
SCK	SCK
SS	/CS
+3.3V	/RESET
GND	GND

4.1 The Hardware Testing

To test the system hardware has made a dirt compound a computer connected to a network router. At the same router connected to the microcontroller system.

Note that the two have formed local network equipment in which each received a static IP address. ENC28J60 controller cannot be obtained automatically from the router IP address.



Figure 5. The system's structure

The ENC28J60 use involves setting a fixed IP address for it, a network mask and gateway address. That address is written in main procedure of software package as a unsigned integer on 8 bits array variable. Same way must be declared the net mask, gateway and DNS servers. Another important variable is the listen port: 80 for TCP and 1200 for UDP.



Figure 6.The Ethernet interface connected at microcontroller

5. The Software Package

The software package contains two components: One for the automation system Ethernet interface; one client to access the system. Several forms client application CAN take. Among These are: Application command prompt type; GUI Application for Windows; accessible HTML page in a browser; Application for Windows Mobile; Java Midlet for mobile phones; Application for smart phone with Android Operating system. Two versions were developed: an HTML page served by accessing microcontroller IP address even his own, and an application for smart phones with Android operating system. The router used to connect computer and microcontroller system is Huawei HG520s. Microcontroller has been allocated via the address 192.168.0.138. Below are some images rendered page served. The microcontroller software development was done in C and compiled with the programming environment called Atmel AVR Studio. The program is designed to consist of two main segments:

Ethernet communication to achieve; system management and implemented control algorithm realized by mcu. In the present study to further comment on the development of network communication routines. To achieve the objective programs designed and tested in the virtual environment provided by Proteus program. After validation program has been adapted and tested microcontroller. Here are the steps algorithm to achieve the program microcontroller to configure ports; to configure the microcontroller SPI communication; to set the configuration registers of the circuit ENC28J60 (addresses, port); is expected to receive a network packet; Package type is detected; If the protocol is IP header is extracted it; if the type is TCP packet handler is calling the package. This routine transmits corresponding HTML page and running and switch pin 7 of port D.Our stack is a collection of different modules. Some modules (such as IP, TCP, UDP and ICMP) must be called when a corresponding packet is received. Any application utilizing our stack must perform certain steps to ensure that modules are called at the appropriate times. This task of managing stack modules remains the same, regardless of main application logic. To decode the package it was developed a stack task. Stack Task is implemented as a cooperative task; when given processing time, it polls the MAC layer for valid data packets. When one is received, it decodes it and routes it to the appropriate module for further processing. The steps used by stacktask are shown in the algorithm below.

6. Ethernet Controller Working:

This board utilizes the new Microchip ENC28J60 Stand-Alone Ethernet Controller IC featuring a host of features to handle most of the network protocol requirements. The board connects directly to most microcontrollers with a standard SPI interface with a transfer speed of up to 20MHz. This module contains what you need to enable Ethernet on your next microcontroller project. It includes a ENC28J60 ethernet controller, an RJ45 socket with link/activity lights and integrated transformer. This Ethernet module enables you to connect a particular embedded device (equipped with SPI support) on to a network. By using this Ethernet module (along with a microcontroller running a small TCP/IP stack) applications like Embedded Web server can be easily developed. Use this module to enable Ethernet interface for your product. It works with any microcontroller operating at 3.3V or 5V. This module works at 5V and is compatible with 5V interface lines. Use SPI process to interface with it. Host web server, ping the module or add it to home automation via internet. Heart of this module is ENC28J60 Ethernet controller from Microchip. This board is assembled with all components as shown in the picture. The circuit board includes all required components for the ethernet controller, plus a 3.3V Voltage Regulator and a RJ-45 jack with integrated transformer and built-in Link and

Activity LEDs for connection to an Ethernet Local Area Network.



ENC28J60 BLOCK DIAGRAM

ENC28J60 BLOCK DIAGRAM

7. Applications

Remote control and monitoring Data capture and logging Industrial Automation Building Automation Appliance remote management Robotics Microcontroller Research and Development TCP/IP Research and Development

8. Proposed Work

Search Engine Optimization has gained a lot of importance as it allows you to use your website to market your products and services on the Internet and drive users and traffic to it. So search engine optimization (SEO) technique in order to find the rating of web and analysing may be tedious and time consuming ,so present physical modules came into existence and a new system has been proposed web page analysis based on the amount of time that web pages visited and the duration will be taken into consideration and gives the statistics.

9. Results and Output:

Here hardware requirements are AVR microcontroller LCD, display,power supply, ENC28j60,ADC,LM 35,Ethernet controller

and software requirements are Code Vision AVR IDE tool Embedded C programming, STK 200/300 programmer.

9.1 Working Principle

In our project we will be using an electromechanical relay, which will be a 5 pin relay and the working of the relay will be like as. The general-purpose relay is rated by the amount of current its switch contacts can handle. Most versions of the general-purpose relay have one to eight poles and can be single or double throw. These are found in computers, copy machines, and other consumer electronic equipment and appliances.

DC Motor Relay

RELAYS: A relay is usually an electromechanical device that is actuated by an electrical current. The current flowing in one circuit causes the opening or closing of another circuit. Relays are like remote control switches and are used in many applications because of their relative simplicity, long life, and proven high reliability. Relays are used in a wide variety of applications throughout industry, such as in telephone exchanges, digital computers and automation systems. Highly sophisticated relays are utilized to protect electric power systems against trouble and power blackouts as well as to regulate and control the generation and distribution of power. In the home, relays are used in refrigerators, washing machines and dishwashers, and heating and air-conditioning controls. Although relays are generally associated with electrical circuitry, there are many other types, such as pneumatic and hydraulic. Input may be electrical and output directly mechanical, or vice versa.

All relays contain a sensing unit, the electric coil, which is powered by AC or DC current. When the applied current or voltage exceeds a threshold value, the coil activates the armature, which operates either to close the open contacts or to open the closed contacts. When a power is supplied to the coil, it generates a magnetic force that actuates the switch mechanism. The magnetic force is, in effect, relaying the action from one circuit to another. The first circuit is called the control circuit; the second is called the load circuit.

On/Off Control: Example: Air conditioning control, used to limit and control a "high power" load, such as a compressor Limit Control:

Example: Motor Speed Control, used to disconnect a motor if it runs slower or faster than the desired speed

10. AVR Studio Software

10.1 ISP Flash Microcontroller Programmer:

Complete In-System Programming Solution for ARM Microcontrollers

Covers All ARM Microcontrollers with In-System Programming Support

Reprogram Both Data Flash and Parameter EEPROM Memories

Complete Schematics for Low-cost In-System Programmer

Simple Three-wire SPI Programming Interface

Working of ISP programmer:

In-System Programming allows programming and reprogramming ARM of any microcontroller Positioned inside the end system. Using a simple threewire SPI interface, the In-System Programmer communicates serially with the ARM microcontroller, reprogramming all non-volatile memories on the chip. In-System Programming eliminates the physical removal of chips from the system. This will save time, and money, both during development in the lab, and when updating the software or parameters in the field. This application note shows how to design the system to support In-System Programming. It also shows how a low-cost In-System Programmer can be made, that will allow the target ARM microcontroller to be programmed from any PC equipped with a regular 9pin serial port. Alternatively, the entire In-System Programmer can be built into the system allowing it to reprogram itself.

11.STEPS FOR CREATING A CODE VISION AVR INTEGRATED DEVELOPMENT ENVIRONMENT:

If we just use compiler and linker independently we still need to get a text editor. So combining everything will actually mess things up. So the best way is to get Software which has it all. That's called an Integrated Development Environment, in short IDE.

I consider Code-Vision-AVR to be the best IDE for getting started with AVR programming on Windows XP, Vista. It has a very good Code Wizard which generate codes automatically you need not mess with the assembly words. So in all my tutorials I will be using CV AVR. You can download evaluation version for free which has code size limitation but good enough for our purpose. For all my examples I will be using Atmega-32 as default microcontroller because it very easily available and is powerful enough with sufficient number of pins and peripherals we use. You can have a look on the datasheet of Atmega-32 in the datasheet section. Let's take a look on the software. The main window looks like following,



Figure 8.CVAVR

PORT is usually a collection of 8 pins. From this tab you can select which pin you want to configure as output and which as input. It basically writes the DDR and PORT register through this setting. Registers are basically RAM locations which configure various peripherals of microcontroller and by changing value of these registers we can change the function it is performing. I will talk more about registers later. So you can configure any pin as output or input by clicking the box. You can also set initial value of the Pins you want to assign or if you are using a pin as input then whether you want to make it as pull-up or tri stated, again I will talk in details about these functions later. Similarly using this code wizard you can very easily configure all the peripherals on the AT mega. Now for generating code just go to File ----> Generate, Save and Exit (of the code wizard) now it will ask you name and location for saving three files. Two being project files and one being the .C file which is your program. try to keep same names of all three files to avoid confusion. By default these files are generated in C:\CVAVR\bin the generated program will open in the text editor. Have a look it has some declarations like PORT, DDR, TCCR0 and many more. These are all registers which configures various functions of AT mega and by changing these value we make different functions. All the details about the registers are commented just below them. Now go down and find following infinite while loop there. We can start writing our part of program just before the while loop. And as for most of the applications we want microcontroller to perform the same task forever we put our part of code in the infinite while loop provided by the code wizard.

Now we want to generate the hex file, so first compile the program. Either press F9 or go to Project ---> Compile. It will show compilation errors if any. If program is error free we can proceed to making of hex file. So either press Shift+F9 or go to Project ----> Make. A pop up window will come with information about code size and flash usage etc. So the machine file is ready now it is in the same folder where we saved those 3 files.

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Figure 9.The result of output window

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/http.c:119: warning: implicit declaratio	n of function 'send zig'
avr-gcc -mmcu=atmega32 -Uall -gdwarf-2	-DF CPU=16000000UL -0s -fsigmed-char -ND -NP -MT arp.o -NF dep/arp.o.d -c/arp.c
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• avr-mcc -mncu=stmens32 -Hall -mdwarf-2	-DF CPU=1600000000Ds -fsigmed-char -ND -NP -MT iggn.oNF den/iggn.o.d -c/iggn.c
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Figure 10. Snapshot of the window after compilation

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Figure 11(a) Generating hex files, AVR ISP1

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0000140 90 7E 89 2	8 87 89 36 9% 36 99 7E CF 24 81 45 81 D~‱+‡°6ĕ6™pÏ\$±E±	
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0000160 86 B9 80 D	C 87 89 08 95 5F 98 FE CF 88 81 8C 81 †'€i‡'D•_}pI(±E±	
0000170 OB 95 5D 9	3 FE CF 8C 89 08 95 FC 01 04 CD 5D 98 D•] >p1024D+02DA] >	
000180 FE CF 31 9	5 80 89 84 91 88 23 09 K7 08 95 KC 01 p11-0°,"#K+0*α0	
000190 04 00 50 9	3 FE UF 31 95 8U 59 8U 51 88 23 U9 FF 10 54 E+23% 1 + E=2	
DODIED DE RE DE R	2 CR BE 01 20 11 E4 04 E0 05 C0 02 20 b/GB/10004/403 0	
000100 DL BF OF B.	1 01 00 02 E0 1E 01 11 05 E0 05 CU 02 30 F(URAL(UDUD BLK,0 1 01 00 02 E0 1E 01 11 05 E0 E5 E0 E0 E0 E3/AUTO_Ausa	
000100 11 F4 50 D	3 00 92 80 92 81 97 98 01 27 58 38 48 / ADD / DBA - ABDA	
0001E0 84 2F 8F 7	1 E9 2F FD ED 88 30 20 F4 E2 OF F3 1F ./Dpé/8ak0 68D6D	
0001F0 80 50 03 0] E2 OF F3 1F 89 5C 80 83 91 50 41 F0 €]□λά⊡ά⊡ά⊖€f'Pl8	
000200 84 E0 76 9	5 67 95 57 95 47 95 8% 95 D1 F7 I8 CF "àv•g·V•G·Š·Ň÷èľ	
000210 CE 01 01 9	5 DE 94 C7 DO 28 96 DF 86 F8 94 DE 8F ÎDD-D°ÇD+-D∰⊗″⊅¿	
0000220 OF BE CD B	7 CF 91 DF 91 08 95 90 BD 89 B9 88 E1 Dk1/1`8'O-Okt'`á	
0000230 81 89 8E D	3 80 80 08 95 98 2F C4 98 85 80 8F 89 йŽė€K⊡•*/Å″.ė⊡≯	
T:		
Erase: Complete - 00:00	0%	
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11(b) AVR ISP Flash programming 2

∲ AIR 159 - Flash:	
File Device View Serial Numbers Help	
Hard Erase price: ATmeps?? - Selup (19441)	
Conv RC Calibration Byte	
Fiel Proton + Field (1)449-456	
Dead + FFEDriv (rhtableF7	
001 Verific & Canada and Art 2017 CED CED CED CED CED CED CED	
001 has 50 Ever 94 47 00 D*600*600*60	
001 WIII 17 1025	
000 Auto Program F5 \$ 47 00 0C 94 47 00 0C 94 47 00 D*GDD*GDD*GDD*GD	
001 Auto Program Options	
DOLUDUSU UC SY YY UU II 24 1F BE CF ES D8 ED DE BF CD BF D°GED\$CNÄÄGAÞįÍį	
00000060 12 ED AO E6 BO E0 E6 E5 FC E1 O2 CO O5 90 OD 92 Dá e ⁰ àzânálàddd'	
00000070 AO 38 B1 07 D9 F7 12 ED AO E8 B2 EO 01 CO 1D 92 8±Dù:Oà è*àDÀD′	
00000080 AF 39 B1 07 E1 F7 0E 94 16 03 OC 94 29 0E OC 94 ⁻ 9±Dá=O"DDD")DD"	
00000090 00 00 98 27 90 77 98 60 95 BB 15 BL 82 95 80 77 CC"/CC"`*xC",*#C	
00000010 88 60 85 88 15 B1 80 E4 9C E9 01 97 F1 #7 08 95 ``_w⊡°€améD-ñ-⊡•	
000000B0 88 E2 0E 94 49 00 88 E2 0E 94 49 00 88 E2 0E 94 'àD"ID'àD"ID'àD"	
000000C0 49 00 86 ED 0E 94 49 00 8C ED 0E 94 49 00 81 ED IDtåD"IDDåD"IDDà	
00000000 0Z 94 49 00 08 95 98 2F 90 7F 9A 60 95 88 15 BA D°IDD+"/DD8`+xD*	
000000ED 82 95 80 7F 8λ 60 85 88 15 8λ 80 E4 9C E9 01 97 ,*€DŠ`.×CP€8xé⊡-	
00000070 F1 F7 08 95 CF 93 DF 93 EC 01 03 CO OE 94 68 00 ñ+0+1"6"100100"H0	
0000010D 21 96 88 81 88 23 D1 F7 DF 91 CF 91 08 95 OE 94 !-'C'∰λ÷β'Ï'C•C"	
00000110 68 00 08 95 81 ED 0E 94 49 00 08 95 80 E8 0E 94 KDD-D&D"IDD-@AD"	
00000120 49 00 08 95 80 EC 0E 94 49 00 08 95 84 E9 0E 94 IDD+€iD*IDD+,éD*	
00000130 49 00 08 95 84 ED 0E 94 49 00 08 95 97 B1 8F 71 IDD+,iD*IDD+-#Dq	
00000140 90 7E 89 2B 87 B9 36 9A 36 99 FE CF 24 B1 45 B1 D+‰+≄^6ĕ6™pÌ\$±E±	
00000150 94 2F 80 ED 30 ED 28 28 39 28 C9 01 D8 95 87 E8 ″/€àDà(+9+ÉE⊡+≉è	
00000160 86 89 80 EC 87 89 08 95 5F 98 FE CF 88 81 8C 81 †*€i‡⊡xpl<±0±	
00000170 08 95 5D 98 FE CF 8C 89 08 95 FC 01 04 CD 5D 98 D+]>þŽ&*D+WEDÅ]>	
00000180 FE CF 31 96 8C 89 84 91 88 23 C9 F7 08 95 FC 01 þÍ1-@',`'#É-D·WD	
00000190 04 CO 5D 9B FE CF 31 96 8C B9 80 81 88 23 C9 F7 DÅ]\bÌ1-@*@D'#Ž+	
000001A0 08 95 DF 93 CF 93 CD B7 DE B7 2B 97 OF B6 F8 94 D+&*1*1+D++D9#"	
000001B0 DE BF OF BE CD BF 81 30 11 F4 94 ED 05 CO 82 30 Þjördjörðdið,0	
000001C0 11 F4 98 E0 01 C0 92 E0 DE 01 11 96 E0 E6 F0 E0 Dô"åDÅ/àBDD-åæðå	
00000100 88 ED 01 90 0D 92 81 50 E1 F7 9E 01 2E 5F 3F 4F (éDDD/DPé÷ÉD?0	
000001E0 84 2F 8F 70 E9 2F F0 E0 8B 30 20 F4 E2 0F F3 1F "/Dpé/6à(0 6áDáD	
000001F0 80 50 03 CO E2 OF F3 1F 89 5C 80 83 91 50 41 FO €]CLÁLÉCU\€f'PL6	
00000200 84 ED 76 95 67 95 57 95 47 95 8∆ 95 D1 #7 E8 CF "åv*g•W•G•Š•Ň÷ėľ	
00000210 CE 01 01 96 0E 94 C7 00 2B 96 0F B6 F8 94 DE BF 10D-C"ÇD+CIQe"₽¿	
00000220 OF BE CD BF CF 91 DF 91 OB 95 90 BD 89 B9 88 E1 DM1;1%D-DM%*'&	
00000230 8A B9 8E E8 80 BD 08 95 98 2F C4 98 85 ED 8F B9 S'Zě¢MI*'/A'.aD'	
HINT:	
Rash File Load File baded (Intellex 8MDS) - 00:00 01	
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Figure 11(c) AVR ISP 3

File Device View Serial Numbers Help	
landware: ARISP v Device: AT maga22 v Seric (200002)	
Cluck Manners FFFE/TM Menners Fines A Lonis Rite Status	
Automatic states and a state and a	
00000000 🚾 94 2% DO OC 94 47 00 OC 94 47 00 DC 94 47 00 D°#DD*GDD*GDD*GDD*GD	
00000010 DC 94 47 00 0C 94 47 00 0C 94 47 00 0C 94 47 00 DC 94 47 00 D"GED"GED"GED"GED	
00000020 0C 95 47 00 0C 9507 020 020 020 020 020 020	
0000000 C 94 47 DO CC 94 47 DO CC 94 47 DO CC 94 47 DO E"GEE"GEE"GE	
00000050 OC 94 47 00 11 24 1F BE CF E5 D8 E0 DE BF CD BF D°GDI\$CNIÅ0àÞįÍ;	
00000060 12 ED AD E6 E0 E6 E5 FC E1 O2 CO O5 90 OD 92 Dà e'àxâuáDÀDDD'	
00000070 AO 38 B1 07 D9 F7 12 ED AO E8 B2 ED 01 CO 1D 92 <u>8HD</u> Ù-CÀ è*ADÀD'	
00000000 AF 39 B1 07 E1 F7 0E 94 16 03 0C 94 29 0E 0C 94 9±04:0"DDD"/DD"	
0000000 00 00 98 27 90 77 98 80 95 88 15 81 82 95 80 77 LL"/LL" **/(", *81	
0000000 88 £5 0£ 04 40 05 85 55 01 04 40 00 88 £5 0£ 04 140.411.40.411.40.411.40.411.	
00000000 00 22 02 91 19 00 00 22 02 91 19 00 00 22 02 91 80 10 80 10 80	
00000000 0E 94 49 00 08 95 98 2F 90 7F 91 60 95 88 15 81 D"IDD+"/DD#`+xD*	
000000E0 82 95 80 77 8Å 60 85 88 15 8Å 80 E4 9C E9 01 97 , €DŠ∵.x⊡*€áxé⊡-	
00000F0 F1 F7 08 95 CF 93 DF 93 EC 01 03 CO 0E 94 68 00 Å+⊡+İ*&*iDDÀD*HD	
00000100 21 96 88 81 88 23 D1 F7 DF 91 CF 91 08 95 OE 94 !-'O'#Ň÷&'Ĭ'O∗O"	
00000110 68 00 08 95 81 E0 0E 94 49 00 08 95 80 E8 0E 94 XDD+DXD*IDD+CHD"	
00000120 49 00 08 95 80 EC 0E 94 49 00 08 95 84 E9 0E 94 IDD+€1D"IDD+,4D"	
00000130 49 00 08 95 84 ED 0E 94 49 00 08 95 97 81 8F 71 100+,10*100+=#10	
00000140 90 12 09 28 00 20 38 20 28 70 01 08 05 82 88 /(@dob/1010000140 90 12 09 20 00 30 80 28 30 28 70 01 08 06 29 10 "****0000140 90 12 09 20 01 29 30 30 30 30 30 10 10 10 10 10 10 10 10 10 10 10 10 10	
00000150 51 21 00 20 30 20 20 20 20 20 20 20 20 20 20 20 20 20	
00000170 08 95 50 98 FE CF 6C 89 08 95 FC 01 04 C0 50 98 D+1>bEd*D+4EDÅ1>	
00000180 FE CF 31 96 8C 89 84 91 88 23 C9 F7 08 95 FC 01 þÍ1-@',''#É÷⊡•#⊡	
00000190 04 CO 5D 98 FE CF 31 96 8C 89 80 81 88 23 C9 F7 DÅ]γþĬ1-@*€D*#É÷	
000001AD D8 95 DF 93 CF 93 CD 87 DE 87 28 97 OF 86 F8 94 D+&^T^f+⊳++O∰e″	
00001BD DE BF OF BE CD BF 81 30 11 F4 94 ED D5 CD 82 30 ⊅/EML2DOD6"ADÅ,O	
000001CD 11 F4 98 ED 01 CO 92 ED DE 01 11 96 ED E6 F0 ED Dô"åDÅ/àBDD-àæðà	
00000100 88 80 01 90 00 92 81 50 81 97 98 01 28 59 39 49 (alb0/UPa-dl, 70	
000001E0 80 51 03 00 E2 0F F0 E0 50 50 20 F1 E2 0F F3 1F "/UBE/BBNO BBUDO D00001E0 80 51 03 00 E2 0F F0 E0 50 50 20 F1 E2 0F F3 1F "/UBE/BBNO BBUDO	
00000200 84 ED 76 95 67 95 57 95 47 95 88 95 D1 F7 EB CF	
00000210 CE 01 01 96 0E 94 C7 00 28 96 OF 86 F8 94 DE 8F ÎDD-D"(D+-D∰s"%¿	
00000220 DF BE CD BF CF 91 DF 91 DE 95 90 BD 89 B9 88 E1 Dw1z1's'D D+%2'á	
00000230 8A B9 8E E8 80 BD 08 95 98 2F C4 98 85 ED 8F B9 Š'Žė€κ⊡•″/Å".èD°	
INT:	
Flash Regramming 74%	
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Figure 11(d) AVR ISP 4

9. Conclusion:

Search engine optimization (SEO) was replaced by web content engagement time in order to find out the rating of different e –publications and e-adds which focuses on the page duration particularly the amount of time users visits the web page. Web Channel platform, promising Web Content Engagement Time (WCET) are recorded, which provides not only integrity and confidence for the publishers and advertisers, but also shines a new light for the future agent-based target marketing and e-reader profile and reading behaviour analysis, for statististical readings its impossible for advertisers to promote in e-publications due to web traffic so it be can be tackle with this proposed system with better results.

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