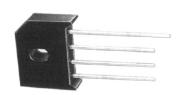
# **RS8 SERIES**

### SINGLE-PHASE SILICON BRIDGE



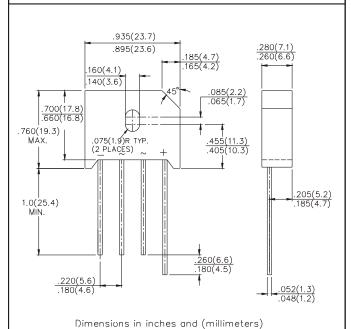


50 TO 1000 VOLTS CURRENT 8.0 Amperes

**VOLTAGE RANGE** 

### FEATURES

- Ideal for printed circuit board
- Realiable low cost construction utilizing molded plastic technique
- Plastic material has underwriters laboratory
   Flammibility Classification 94V-0
- Surge overload rating:300 amperes peak
- · Mounting Torgue: 5 In. lb. max
- UL recognized file # E149311



#### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified. Resistive or inductive load, 60 Hz. For capacitive load, derate current by 20%.

		RS8005	RS801	RS802	RS804	RS806	RS808	RS810	UNITS
Maximum Recurrent Peak Reverse Voltage	$V_{RRM}$	50	100	200	400	600	800	1000	٧
Maximum RMS Bridge Input Voltage	V <sub>RMS</sub>	35	70	140	280	420	560	700	٧
Maximum DC Blocking Voltage	$V_{DC}$	60	100	200	400	600	800	1000	٧
Maximum Average Forward @ T <sub>C</sub> =100°C Output Current @ T <sub>A</sub> =65°C	V <sub>(AV)</sub>	8.0 8.0							A A
Peak Forward Surge Current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	200							А
Maximum DC Forward Voltage drop per element at 4.0A DC	V <sub>F</sub>	1							٧
Maximum DC Reverse Current at rated @ T <sub>A</sub> =25°C DC Blocking Voltage Per Element @ T <sub>A</sub> =100°C	I <sub>R</sub>	10							μ <b>Α</b>
		1							mA
Maximum Themal Resistance (Note)	$R\theta JC$	4.7							°C/W
Operating Temperature Range	T <sub>J</sub>	-55 to +125							°C
Storage Temperature Range	T <sub>STG</sub>	-55 to +150							°C

# **RS8 SERIES**

### SINGLE-PHASE SILICON BRIDGE



## RATING AND CHARACTERISTICS CURVES RS8 SERIES

OUTPUT RECTIFIED CURRENT

8

WOUNTING

HEAT-SINK
MOUNTING

COPER PC BOARD-TA

0,51 1.27mm LEAD LENGTH

TEMPERATURE, °C

Fig.1 - DERATING CURVE

Fig.3 - MAXIMUM NON-REPETITIVE PEAK
FORWARD SURGE CURRENT

250
200
150
8.3ms Sinje Half Sine Wave
JEDEC Method

25
0
1 10 100

NUMBER OF CYCLES AT 60 Hz

Fig.2 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

**ELECTRONIC** 

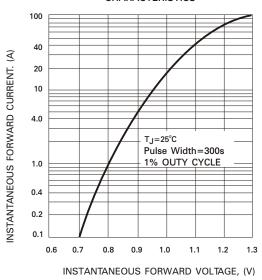


Fig.4 - TYPICAL REVERSE

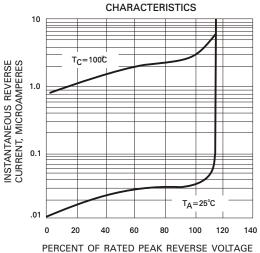
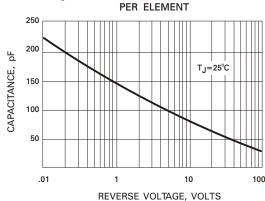


Fig.5 - TYPICAL JUNCTION CAPACITANCE



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