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Vishay Semiconductors

Standard Avalanche Sinterglass Diode

FEATURES

Glass passivated junctionHermetically sealed package

• Material categorization:

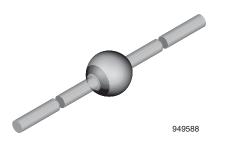
• High voltage rectification

APPLICATIONS

www.vishay.com/doc?99912

for definitions of compliance please see

· Efficiency diode in horizontal deflection circuit



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DESIGN SUPPORT TOOLS



MECHANICAL DATA

Case: SOD-64

Terminals: plated axial leads, solderable per MIL-STD-750, method 2026

Polarity: color band denotes cathode end

Mounting position: any

Weight: approx. 858 mg

ORDERING INFORMATION (Example)						
DEVICE NAME	ORDERING CODE	TAPED UNITS	MINIMUM ORDER QUANTITY			
BY228	BY228TR	2500 per 10" tape and reel	12 500			
BY228	BY228TAP	2500 per ammopack	12 500			

PARTS TABLE		
PART	TYPE DIFFERENTIATION	PACKAGE
BY228	V _R = 1500 V; I _{F(AV)} = 3 A	SOD-64

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
Reverse voltage	See electrical characteristics	BY228	V _R	1500	V
Repetitive peak reverse voltage	l _R = 100 μA		V _{RRM}	1650	V
Peak forward surge current	t _p = 10 ms, half sine wave		I _{FSM}	50	А
Average forward current			I _{F(AV)}	3	А
Junction temperature			Тj	140	°C
Storage temperature range			T _{stg}	-55 to +175	°C
Non repetitive reverse avalanche energy	$I_{(BR)R} = 0.4 A$		E _R	10	mJ

MAXIMUM THERMAL RESISTANCE (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Junction ambient	On PC board with spacing 25 mm	R _{thJA}	70	K/W	

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BY228

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I _F = 5 A	V _F	-	-	1.5	V
Reverse current	V _R = 1500 V	I _R	-	2	5	μA
neverse current	V _R = 1500 V, T _j = 140 °C	I _R	-	-	140	μA
Reverse recovery time	I _F = 0.5 A, I _R = 1 A, i _R = 0.25 A	t _{rr}	-	-	2	μs
Total reverse recovery time	I _F = 1 A, - dI _F /dt = 0.05 A/μs	t _{rr}	-	-	20	μs

TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

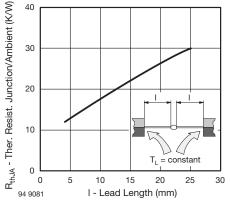


Fig. 1 - Typ. Thermal Resistance vs. Lead Length

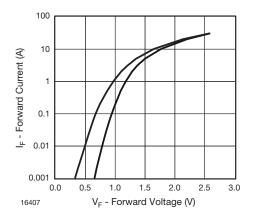


Fig. 2 - Forward Current vs. Forward Voltage

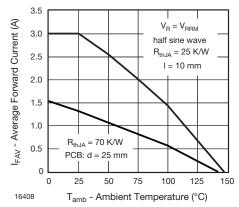


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

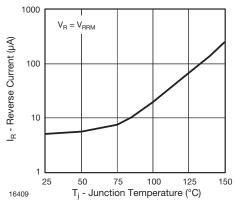


Fig. 4 - Reverse Current vs. Junction Temperature

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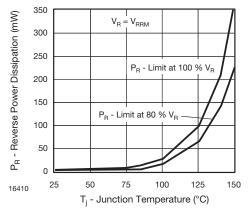


Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature

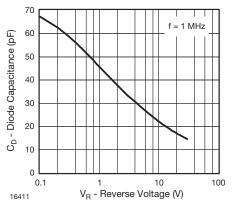
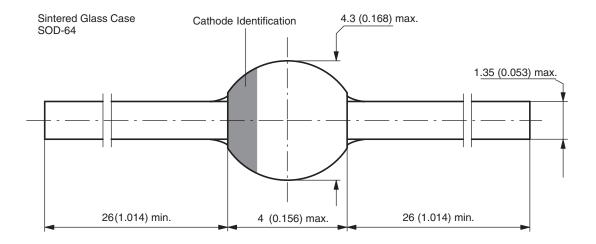


Fig. 6 - Diode Capacitance vs. Reverse Voltage

PACKAGE DIMENSIONS in millimeters (inches): SOD-64



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