

FUJI POWER MOSFET

Super J MOS[®] S1 series

N-Channel enhancement mode power MOSFET

Features

Outline Drawings [mm] Low on-state resistance Low switching loss

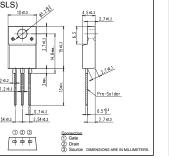
easy to use (more controllabe switching dV/dt by R_g)

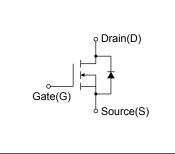
Applications

UPS Server Telecom Power conditioner system Power supply

TO-220F(SLS) 2.7±0.2 £ 12 Pre-Solder 0.5 8.3 07±0.2 2.54±0.2 2.7 10.2 2.54 #0.2 123 Gate Drain -----







Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)

Description	Symbol	Characteristics	Unit	Remarks
Drain Source Veltage	VDS	600	V	
Drain-Source Voltage	VDSX	600	V	V _{GS} =-30V
Continuous Drain Current	lo Rola	135 F	A	Tc=25°C Note*1
Continuous Drain Current		102-1287月日	A	Tc=100°C Note*1
Pulsed Drain Current	IDP/	5 105 ±105 4 P	A	Note*1
Gate-Source Voltage	VGs ATT D	5 × (±30	V	
Repetitive and Non-Repetitive Maximum Avalanche Current	Tar	diffet	А	Note *2
Non-Repetitive Maximum Avalanche Energy	EAST CE PI	1239,6 1239,6	す₀ mJ	Note *3
Maximum Drain-Source dV/dt	dVos/dt	5願し、50	νγγμs	V _{DS} ≤ 600V
Peak Diode Recovery dV/dt	dV404 table	igning80	kV/µs	Note *4
Peak Diode Recovery - di/dt	-di/dt DevN OC-	100	A/µs	Note *5
Maximum Bower Discingtion tull設計してなり	P.	2.16	W	T₂=25°C
Maximum Power Dissipation 注:新規設計になります Operating and Storage Temperature range		110	vv	Tc=25°C
Operating and Storage Temperatury Offe	Tch	150	°C	
Operating and Storage remperature range	Tstg	-55 to +150	°C	

 Note *1 : Limited by maximum channel temperature.

 Note *2 : Tch≤150°C, See Fig.1 and Fig.2

 Note *3 : Starting Tch=25°C, IAs=4A, L=142mH, Vbb=60V, Rg=50Ω, See Fig.1 and Fig.2

 EAS limited by maximum channel temperature and avalanche current.

 Note *4 : Ir≤-lb, -di/dt=100A/µs, Vbs peak≤600V, Tch≤150°C.

 Note *5 : Ir≤-lb, dV/dt=30kV/µs, Vbs peak≤600V, Tch≤150°C.

Electrical Characteristics at Tc=25°C (unless otherwise specified) Static Ratings

Description	Symbol	Conditions		min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250μA V _{GS} =0V		600	-	-	V
Gate Threshold Voltage	V _{GS(th)}	I _D =1.3mA V _{DS} =V _{GS}		3.0	4.0	5.0	V
Zero Gate Voltage Drain Current		V _{DS} =600V V _{GS} =0V	T _{ch} =25°C	-	-	25	-μA
	IDSS	V _{DS} =480V V _{GS} =0V	T _{ch} =125°C	-	190	-	
Gate-Source Leakage Current	lgss	V _{GS} = ± 30V V _{DS} =0V		-	10	100	nA
Drain-Source On-State Resistance	RDS(on)	I _D =17.5A V _{GS} =10V		-	0.089	0.105	Ω
Gate resistance	Rg	f=1MHz, open drain		-	1.1	-	Ω

Dynamic Ratings

Description	Symbol	Conditions	min.	typ.	max.	Unit
Forward Transconductance	g _{fs}	I _D =17.5A V _{DS} =25V	13.5	27	-	S
Input Capacitance	Ciss	V _{DS} =400V V _{GS} =0V f=250kHz	-	2530	-	
Output Capacitance	Coss		-	75	-	
Reverse Transfer Capacitance	Crss		-	5.5	-	
Effective output capacitance, energy related (Note *6)	C _{o(er)}	V _{GS} =0V V _{DS} =0400V	-	195	-	pF
Effective output capacitance, time related (Note *7)	C _{o(tr)}	V _{GS} =0V V _{DS} =0400V D=constant	670	-		
td(on)	td(on)	V _{DD} =400V, V _{GS} =10V I _D =17.5A, R₀=18Ω See Fig.3 and Fig.4	-	116	-	ns
Turn-On Time	tr		-	28	-	
td(off)	td(off)		-	163	-	
Turn-Off Time	tr		-	18	-	
Total Gate Charge	QG		-	92	-	
Gate-Source Charge	Q _{GS}	V _{DD} =400V, I _D =35A	-	24.5	-	
Gate-Drain Charge	QGD	─ V _{GS} =10V _ See Fig.5	-	38	-	nC
Drain-Source crossover Charge	Qsw		-	13	-	1

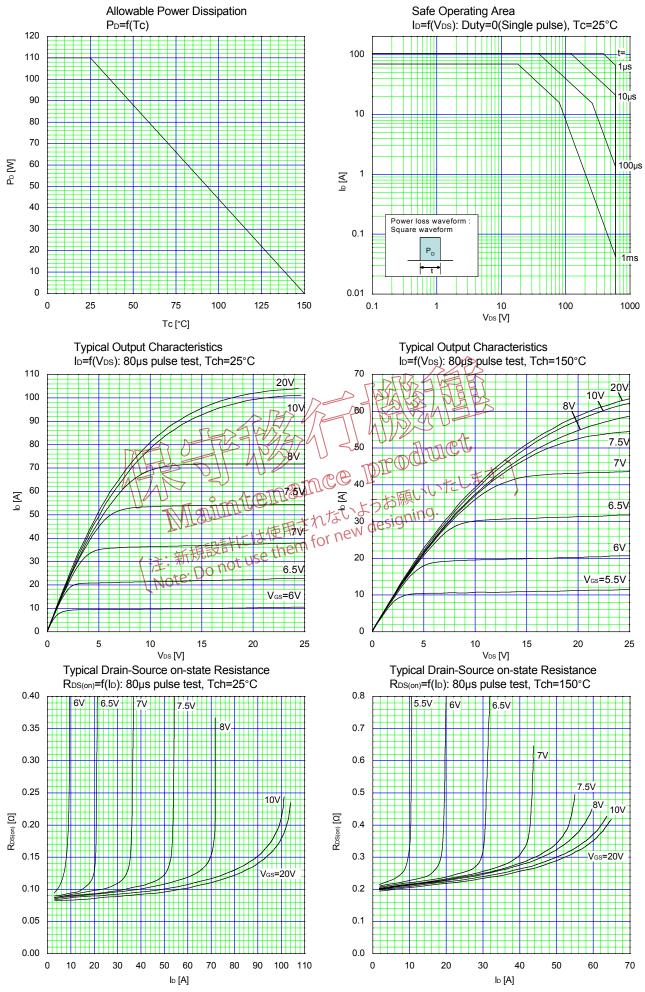
Note *6 : $C_{o(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 400V. Note *7 : $C_{o(tr)}$ is a fixed capacitance that gives the same charging times as C_{oss} while V_{DS} is rising from 0 to 400V.

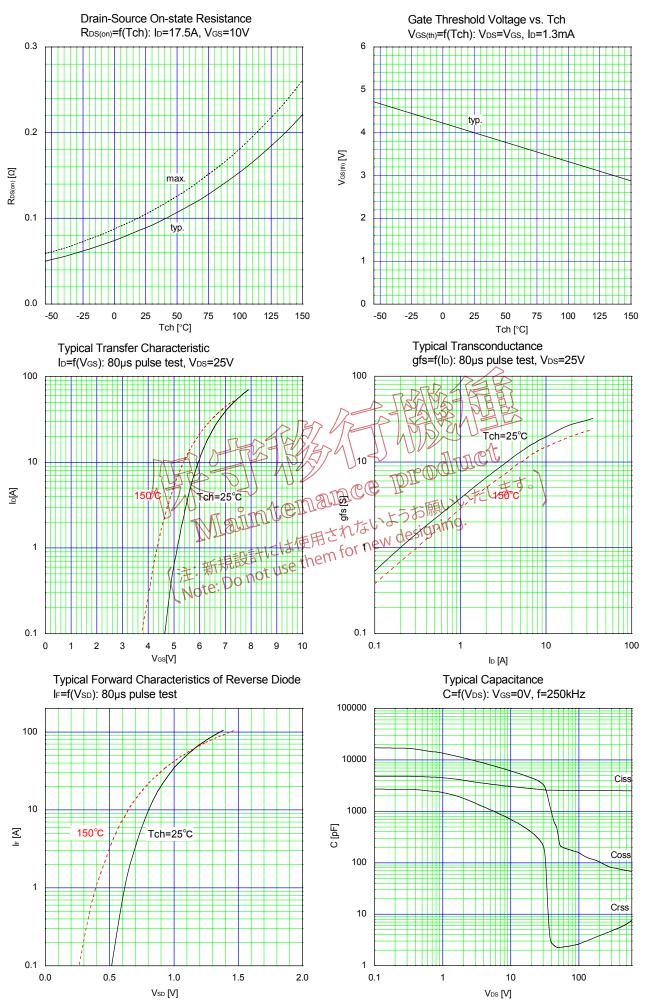
Reverse Diode

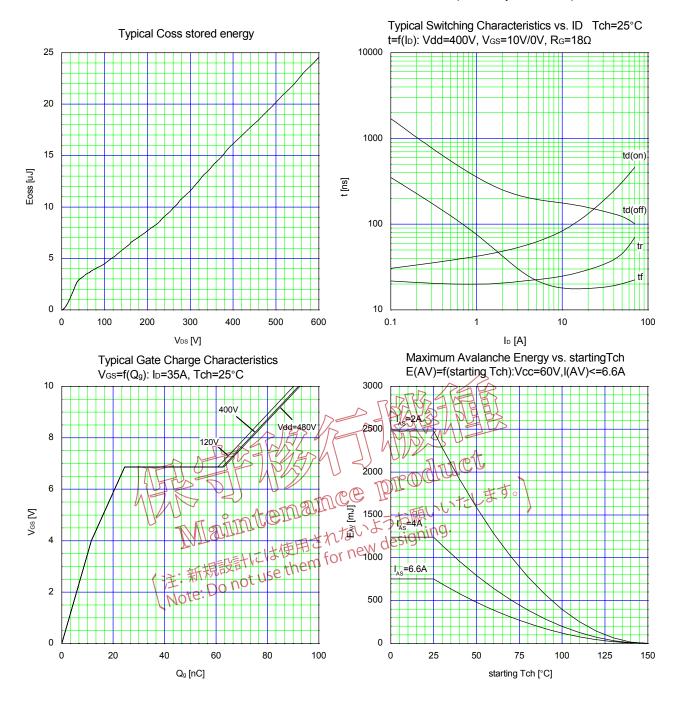
Description	Symbol	Conditions	e min	typ.	max.	Unit
Avalanche Capability	IAV R	L=31.6mH,T==25°C See Fig. 1 and/Fig.2	6.6 1 0 1	-	-	A
Diode Forward On-Voltage	TRED D	II=35A,Wes=0V Ten=25°C	anuic o	#]	1.35	V
Reverse Recovery Time	to the second se	The 25°CU PULL The 25°CU PULL	vv <u>te</u>	185	-	ns
Reverse Recovery Charge	o.M.Lai	-di/dt=100A/us Re=15001 th=25°C for new design	<u>ng.</u>	1.3	-	μC
Peak Reverse Recovery Current	泄.新規副	not USE thigh in the	-	14	-	А

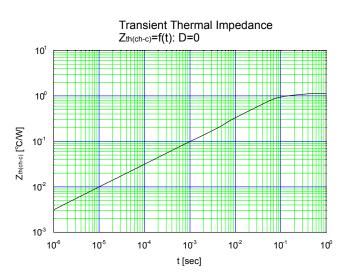
Thermal Resistance

Parameter	Symbol	min.	typ.	max.	Unit
Channel to Case	Rth(ch-c)	-	-	1.14	°C/W
Channel to Ambient	Rth(ch-a)	-	-	58	°C/W









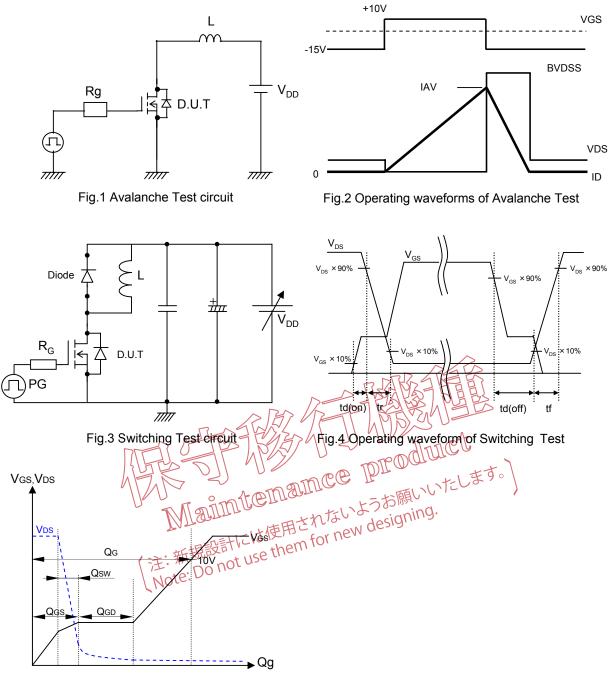


Fig.5 Operating waveform of Gate charge Test

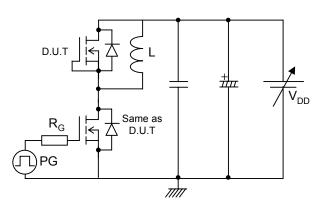


Fig.6 Reverse recovery Test circuit

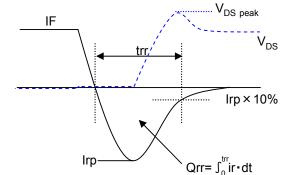
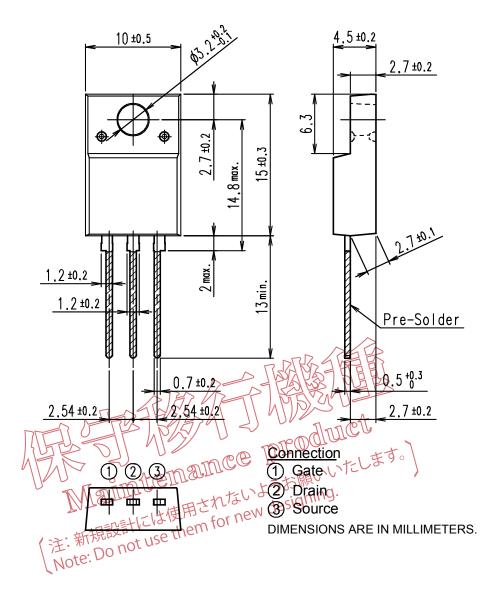
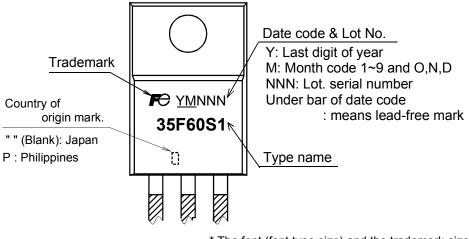


Fig.7 Operating waveform of Reverse recovery Test

Outview: TO-220F(SLS) Package



Marking



* The font (font type,size) and the trademark-size might be actually different.

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