

# 2SK3595-01MR

## FUJI POWER MOSFET Super FAP-G Series

### N-CHANNEL SILICON POWER MOSFET

#### ■ Features

- High speed switching
- Low on-resistance
- No secondary breakdown
- Low driving power
- Avalanche-proof

#### ■ Applications

- Switching regulators
- UPS (Uninterruptible Power Supply)
- DC-DC converters

#### ■ Maximum ratings and characteristic Absolute maximum ratings

● (Tc=25°C unless otherwise specified)

Item	Symbol	Ratings	Unit
Drain-source voltage	V <sub>DS</sub>	200	V
	V <sub>DSX</sub> *5	170	V
Continuous drain current	I <sub>D</sub>	±45	A
Pulsed drain current	I <sub>D(puls)</sub>	±180	A
Gate-source voltage	V <sub>GS</sub>	±30	V
Non-repetitive Avalanche current	I <sub>AS</sub> *2	45	A
Maximum Avalanche Energy	E <sub>AS</sub> *1	258.9	mJ
Maximum Drain-Source dV/dt	dV <sub>DS</sub> /dt *4	20	kV/μs
Peak Diode Recovery dV/dt	dV/dt *3	5	kV/μs
Max. power dissipation	P <sub>D</sub>	T <sub>a</sub> =25°C	2.16
		T <sub>c</sub> =25°C	95
Operating and storage temperature range	T <sub>ch</sub>	+150	°C
	T <sub>stg</sub>	-55 to +150	°C
Isolation voltage	V <sub>ISO</sub> *6	2	

\*1 L=205μH, V<sub>CC</sub>=48V, T<sub>ch</sub>=25°C, See to Avalanche Energy Graph \*2 T<sub>ch</sub> ≤ 50°C

\*3 I<sub>F</sub> ≤ -I<sub>D</sub>, -di/dt=50A/μs, V<sub>CC</sub> ≤ BV<sub>DSS</sub>, T<sub>ch</sub> ≤ 150°C \*4 V<sub>DS</sub> ≤ 200V \*5 V<sub>GS</sub>=-30V \*6 t=60sec f=60Hz

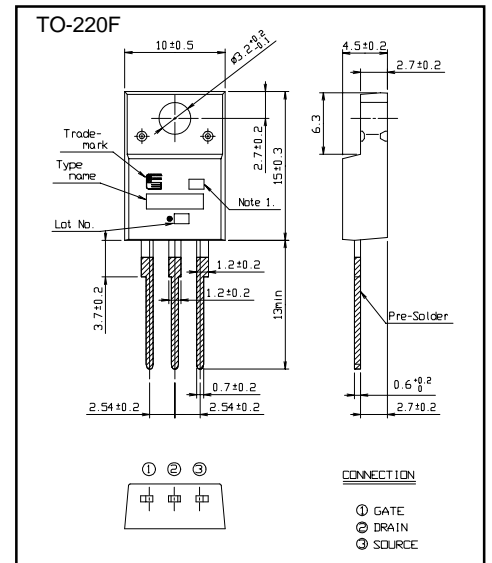
#### ● Electrical characteristics (Tc =25°C unless otherwise specified)

Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> =250μA V <sub>GS</sub> =0V	200			V
Gate threshold voltage	V <sub>GS(th)</sub>	I <sub>D</sub> =250μA V <sub>DS</sub> =V <sub>GS</sub>	3.0		5.0	V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> =200V V <sub>GS</sub> =0V			25	μA
		V <sub>DS</sub> =160V V <sub>GS</sub> =0V			250	
Gate-source leakage current	I <sub>GSS</sub>	V <sub>GS</sub> =±30V V <sub>DS</sub> =0V		10	100	nA
Drain-source on-state resistance	R <sub>DS(on)</sub>	I <sub>D</sub> =15A V <sub>GS</sub> =10V		50	66	mΩ
Forward transconductance	g <sub>fs</sub>	I <sub>D</sub> =15A V <sub>DS</sub> =25V	12.5	25		S
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> =75V		1960	2940	pF
Output capacitance	C <sub>oss</sub>	V <sub>GS</sub> =0V		260	390	
Reverse transfer capacitance	C <sub>rss</sub>	f=1MHz		18	27	
Turn-on time t <sub>on</sub>	td(on)	V <sub>CC</sub> =48V I <sub>D</sub> =15A		20	30	ns
	t <sub>r</sub>	V <sub>GS</sub> =10V		17	26	
Turn-off time t <sub>off</sub>	td(off)	R <sub>GS</sub> =10 Ω		53	80	
	t <sub>f</sub>			19	29	
Total Gate Charge	Q <sub>G</sub>	V <sub>CC</sub> =100V		51	76.5	nC
Gate-Source Charge	Q <sub>GS</sub>	I <sub>D</sub> =30A		15	22.5	
Gate-Drain Charge	Q <sub>GD</sub>	V <sub>GS</sub> =10V		16	24	
Avalanche capability	I <sub>AV</sub>	L=205μH T <sub>ch</sub> =25°C	45			A
Diode forward on-voltage	V <sub>SD</sub>	I <sub>F</sub> =30A V <sub>GS</sub> =0V T <sub>ch</sub> =25°C		1.10	1.65	V
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> =30A V <sub>GS</sub> =0V		0.19		μs
Reverse recovery charge	Q <sub>rr</sub>	-di/dt=100A/μs T <sub>ch</sub> =25°C		1.4		μC

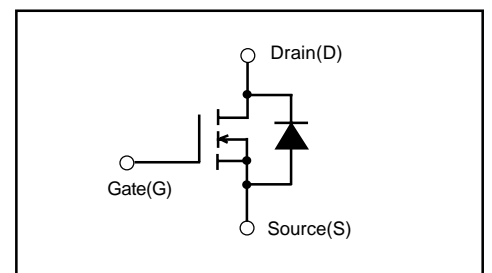
#### ● Thermal characteristics

Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal resistance	R <sub>th(ch-c)</sub>	channel to case			1.316	°C/W
	R <sub>th(ch-a)</sub>	channel to ambient			300	°C/W

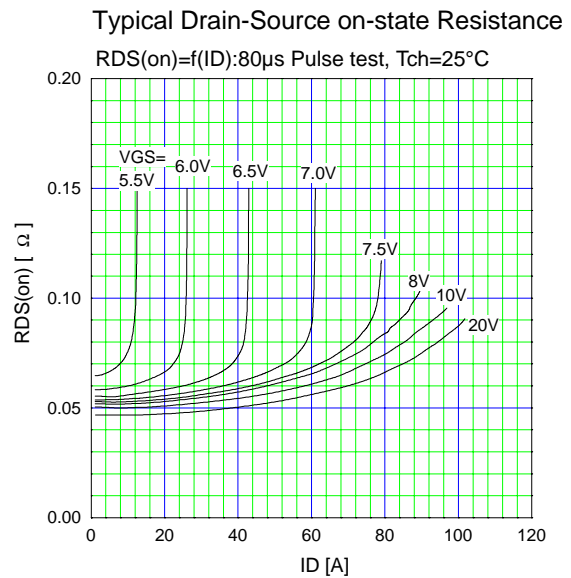
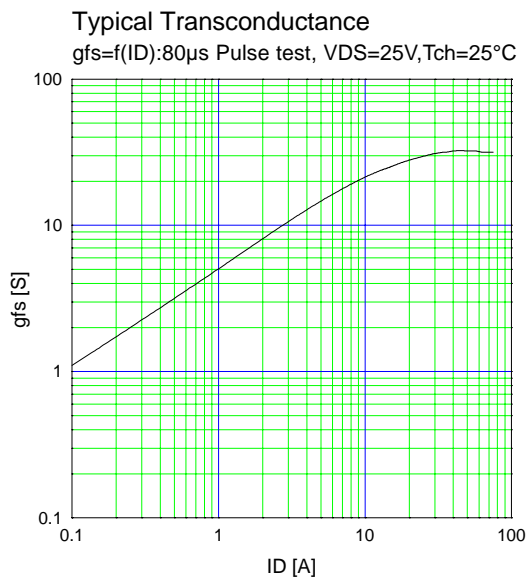
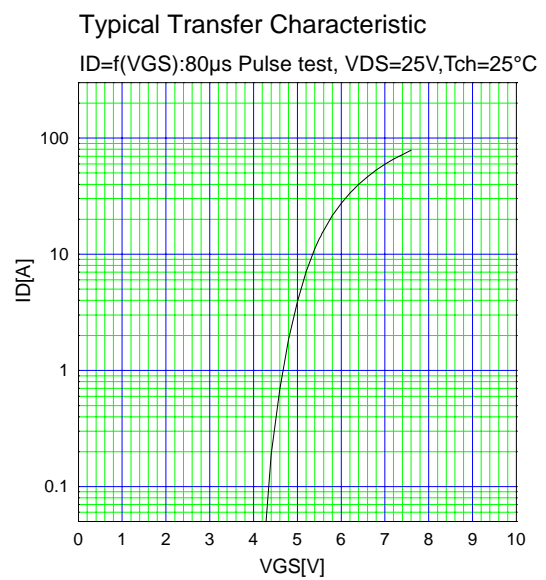
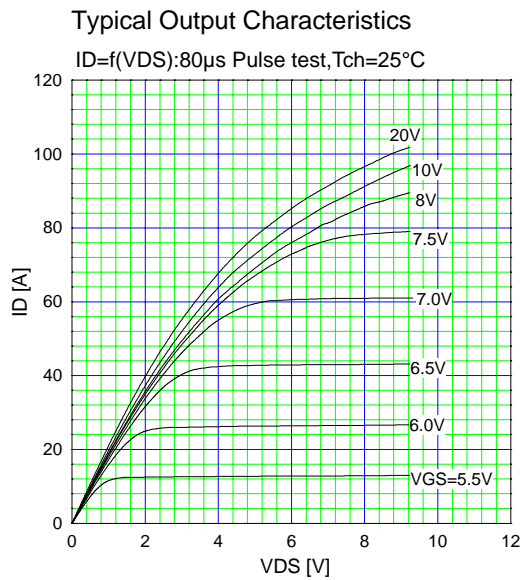
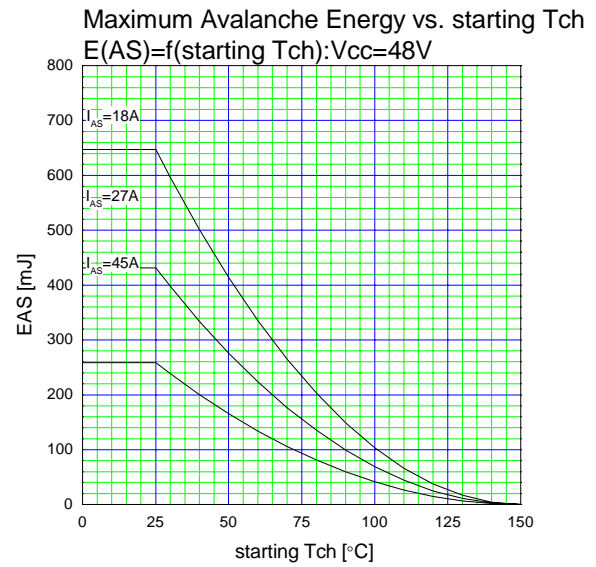
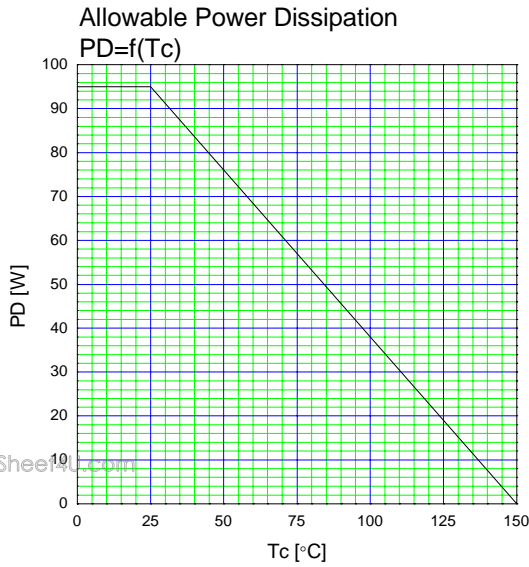
#### ■ Outline Drawings (mm)



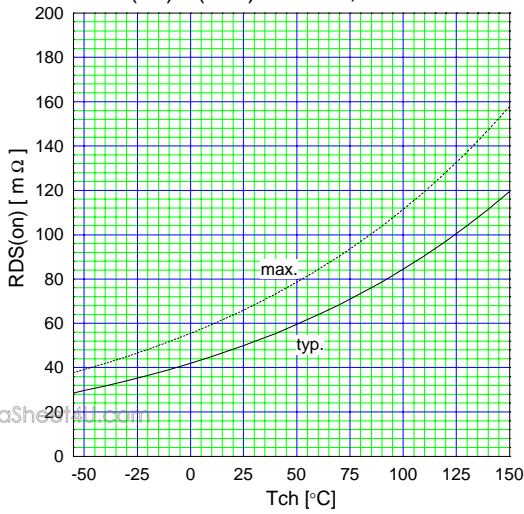
#### ■ Equivalent circuit schematic



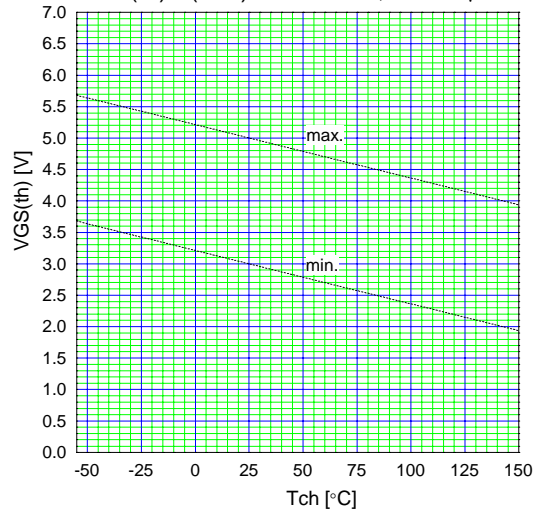
## Characteristics



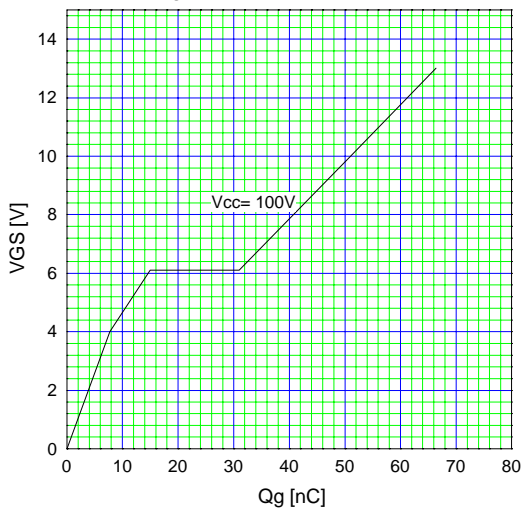
Drain-Source On-state Resistance  
 $R_{DS(on)} = f(T_{ch}): I_D = 15A, V_{GS} = 10V$



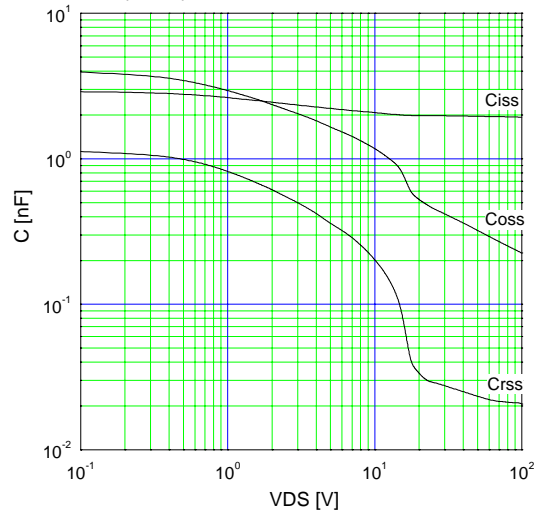
Gate Threshold Voltage vs.  $T_{ch}$   
 $V_{GS(th)} = f(T_{ch}): V_{DS} = V_{GS}, I_D = 250\mu A$



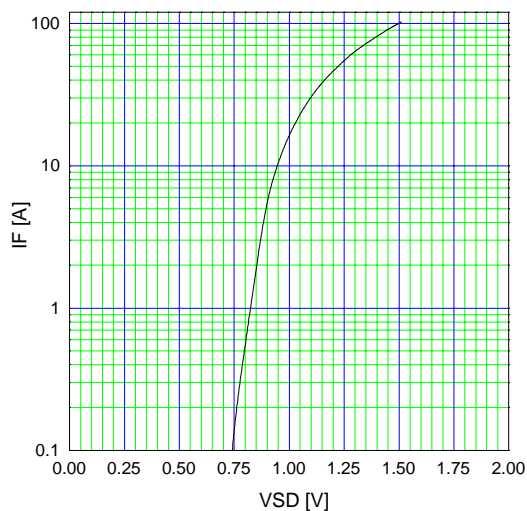
Typical Gate Charge Characteristics  
 $V_{GS} = f(Q_g): I_D = 30A, T_{ch} = 25^\circ C$



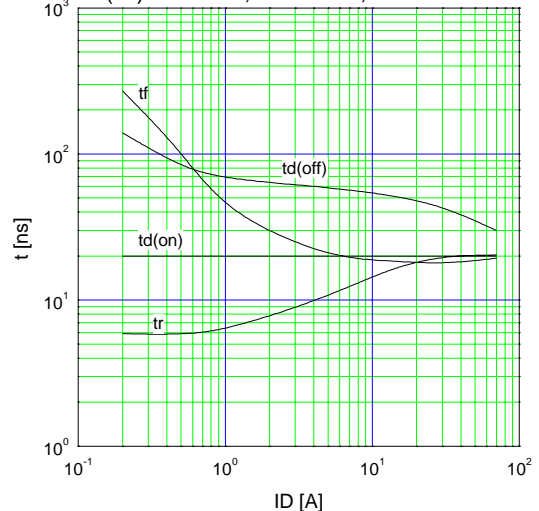
Typical Capacitance  
 $C = f(V_{DS}): V_{GS} = 0V, f = 1MHz$



Typical Forward Characteristics of Reverse Diode  
 $I_F = f(V_{SD}): 80\mu s$  Pulse test,  $T_{ch} = 25^\circ C$

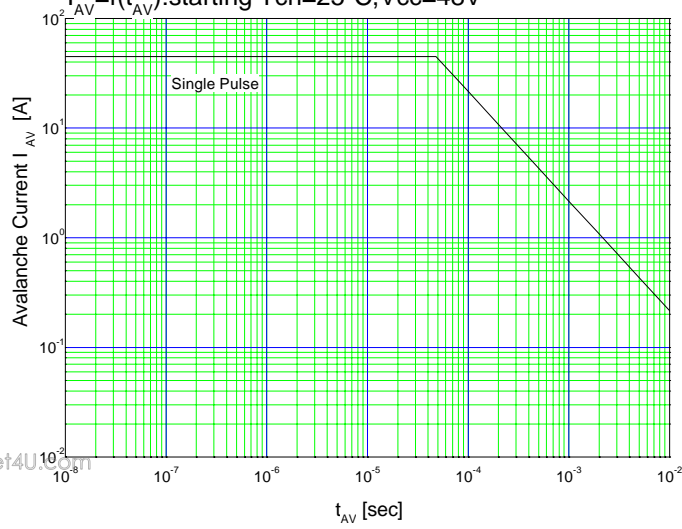


Typical Switching Characteristics vs.  $I_D$   
 $t = f(I_D): V_{CC} = 48V, V_{GS} = 10V, R_G = 10\Omega$



Maximum Avalanche Current Pulsewidth

$I_{AV} = f(t_{AV})$ : starting  $T_{ch} = 25^{\circ}\text{C}$ ,  $V_{cc} = 48\text{V}$



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Maximum Transient Thermal Impedance

$Z_{th}(ch-c) = f(t)$ :  $D = 0$

