Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (DTMOS II)

TK13A65U

Switching Regulator Applications

• Low drain-source ON resistance: RDS (ON) = 0.32Ω (typ.)

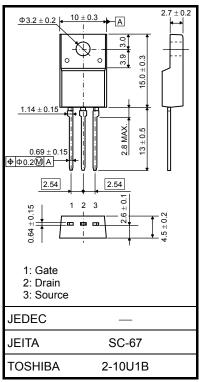
• High forward transfer admittance: $|Y_{fs}| = 8.0 \text{ S (typ.)}$

• Low leakage current: $I_{DSS} = 100 \,\mu\text{A} \,(\text{max}) \,(V_{DS} = 650 \,\text{V})$

• Enhancement-mode: $V_{th} = 3.0 \text{ to } 5.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	650	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	ID	13	۸	
	Pulse (Note 1)	I _{DP}	26	А	
Drain power dissipati	on (Tc = 25°C)	P _D	40	W	
Single pulse avalance	ne energy (Note 2)	E _{AS}	86	mJ	
Avalanche current		I _{AR}	13	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	4.0	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

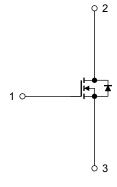
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	3.125	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25 °C (initial), L = 0.9 mH, R_G = 25 Ω , I_{AR} = 13 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.



Internal Connection

Start of commercial production 2009-03

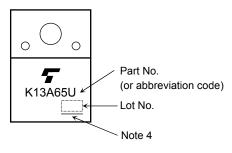
Electrical Characteristics (Ta = 25°C)

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off current		I _{DSS}	V _{DS} = 650 V, V _{GS} = 0 V	_	_	100	μА
Drain-source breakdown voltage		V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	650	_	_	V
Gate threshold vo	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	3.0	_	5.0	V
Drain-source ON	resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 6.5 A	_	0.32	0.38	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 6.5 A	2.0	8.0	_	S
Input capacitance		C _{iss}		_	950	_	pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	47	_	
Output capacitance		C _{oss}		_	2300	_	
Switching time	Rise time	t _r	V_{GS} V_{OV} V	_	30		
	Turn-ON time	t _{on}			65	_	ns
	Fall time	t _f	/// V _{DD} ≈ 200 V	_	8		115
	Turn-OFF time	t _{off}	Duty ≤ 1%, t _w = 10 μs	_	80	_	
Total gate charge		Qg		_	17	_	
Gate-source charge		Q _{gs}	V _{DD} ≈ 400 V, V _{GS} = 10 V, I _D = 13 A	_	10	_	nC
Gate-drain charge		Q _{gd}		_	7	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

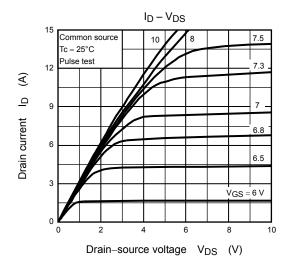
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	13	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	26	Α
Forward voltage (diode)	V_{DSF}	I _{DR} = 13 A, V _{GS} = 0 V	_	_	-1.7	٧
Reverse recovery time	t _{rr}	I _{DR} = 13 A, V _{GS} = 0 V,	_	430	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 100 A/μs	_	7.0	_	μС

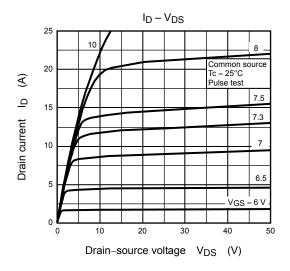
Marking

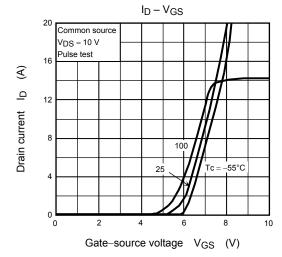


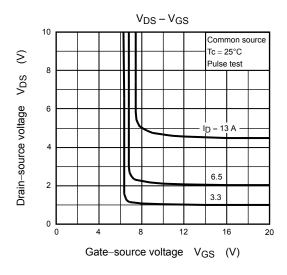
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

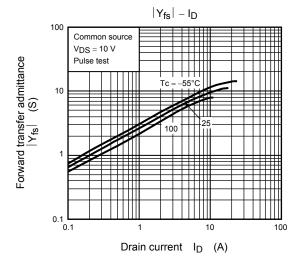
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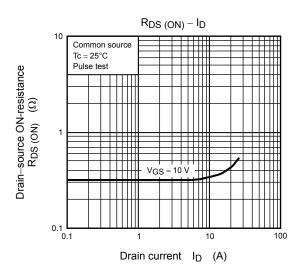


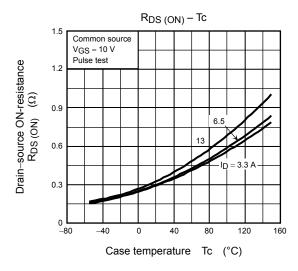


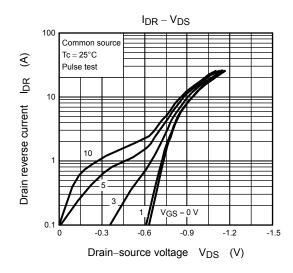


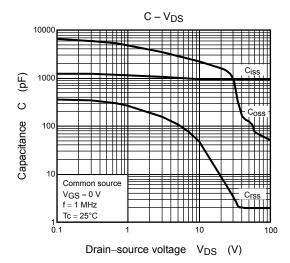


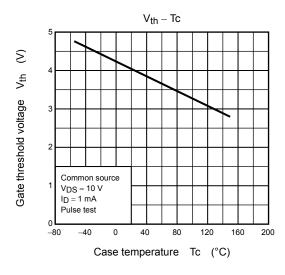


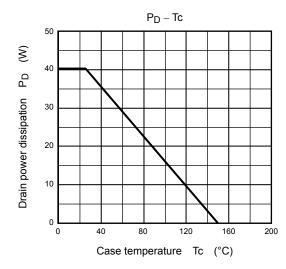


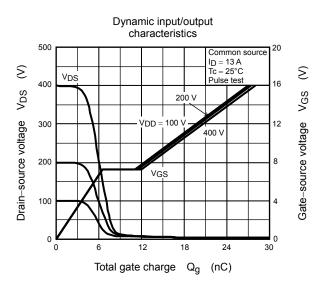


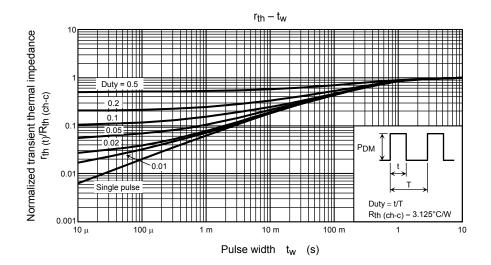


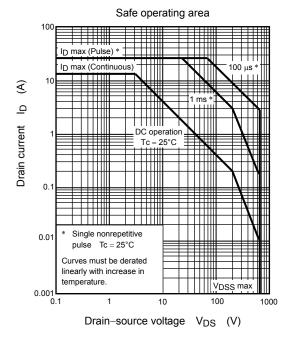


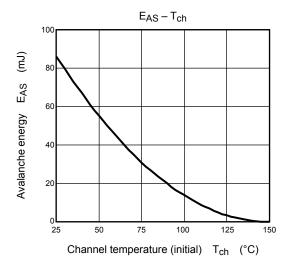


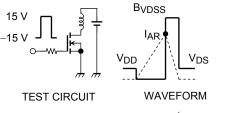












$$R_G = 25~\Omega$$

$$V_{DD} = 90~V,~L = 0.9~mH$$

$$\mathsf{E}_{AS} \!=\! \frac{1}{2} \!\cdot\! L \!\cdot\! I^2 \!\cdot\! \left(\! \frac{\mathsf{BVDSS}}{\mathsf{BVDSS} - \mathsf{VDD}} \right)$$

5 2013-11-01

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