

High Efficient Surface Mount Rectifiers

FEATURES

- Low power loss, high efficiency
- Ideal for automated placement
- Glass passivated junction chip.
- Fast switching for high efficiency
- Moisture sensitivity level: level 1, per J-STD-020
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

MECHANICAL DATA

Case: DO-214AA (SMB)





DO-214AA (SMB)

Molding compound, UL flammability classification rating 94V-0 Base P/N with suffix "G" on packing code - green compound (halogen-free) Base P/N with prefix "H" on packing code - AEC-Q101 qualified **Terminal:** Matte tin plated leads, solderable per JESD22-B102 Meet JESD 201 class 1A whisker test with prefix "H" on packing code meet JESD 201 class 2 whisker test **Polarity:** Indicated by cathode band **Weight:** 0.093 g (approximately)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS (T _A =25°C unless otherwise noted)										
PARAMETER	SYMBOL	HS	HS HS HS HS HS H		HS	HS HS HS				
PARAMETER	STIVIBOL	2A	2B	2D	2F	2G	2J	2K	2M	UNIT
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	200	300	400	600	800	1000	V
Maximum RMS voltage	V _{RMS}	35	70	140	210	280	420	560	700	V
Maximum DC blocking voltage	V _{DC}	50	100	200	300	400	600	800	1000	V
Maximum average forward rectified current	I _{F(AV)}	2 A								
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	50 A								
Maximum instantaneous forward voltage (Note 1) I _F = 2 A	V _F	1.0 1.3 1.7			V					
Maximum reverse current @ rated VR $T_J=25 \ ^{\circ}C$ $T_J=125 \ ^{\circ}C$	I _R	5 150 μΑ			μA					
Maximum reverse recovery time (Note 2)	Trr	50 75		ns						
Typical junction capacitance (Note 3)	Cj	50 30				pF				
Typical thermal resistance	R _{θJA}	80 ^o C/v		^o C/W						
Operating junction temperature range	TJ	- 55 to +150 ^o C								
Storage temperature range	T _{STG}	- 55 to +150 ^o C								
	-	-								-

Note 1: Pulse test with PW=300µs, 1% duty cycle

Note 2: Reverse Recovery Test Conditions: I_F =0.5A, I_R =1.0A, I_{RR} =0.25A

Note 3: Measured at 1 MHz and Applied V_R =4.0 Volts



HS2A thru HS2M

Taiwan Semiconductor

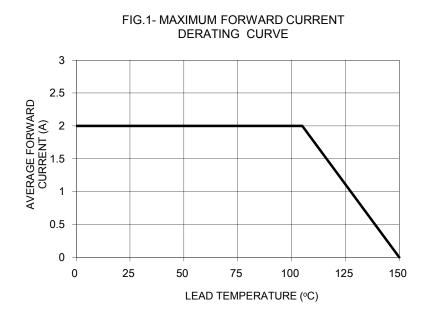
ORDERING INFORMATION						
PART NO.	AEC-Q101	PACKING	GREEN COMPOUND	PACKAGE	PACKING	
	QUALIFIED	CODE	CODE			
11000		R5		SMB	850 / 7" Plastic reel	
HS2x (Note 1)	Prefix "H"	R4	Suffix "G"	SMB	3,000 / 13" Paper reel	
		M4		SMB	3,000 / 13" Plastic reel	

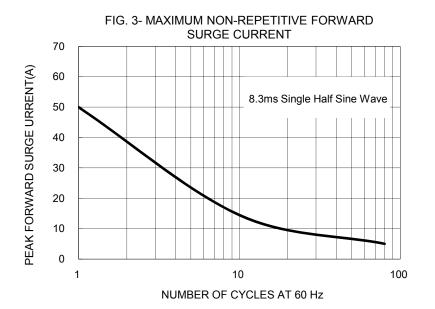
Note 1: "x" defines voltage from 50V (HS2A) to 1000V (HS2M)

EXAMPLE							
PREFERRED P/N	PART NO.	AEC-Q101 QUALIFIED	PACKING CODE	GREEN COMPOUND CODE	DESCRIPTION		
HS2M R5	HS2M		R5				
HS2M R5G	HS2M		R5	G	Green compound		
HS2MHR5	HS2M	Н	R5		AEC-Q101 qualified		

RATINGS AND CHARACTERISTICS CURVES

(TA=25°C unless otherwise noted)





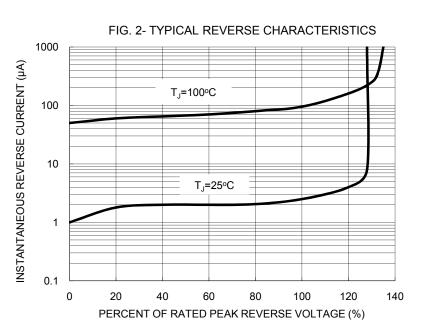


FIG. 5- TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

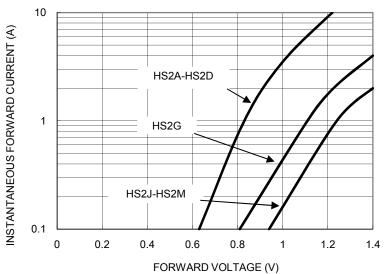
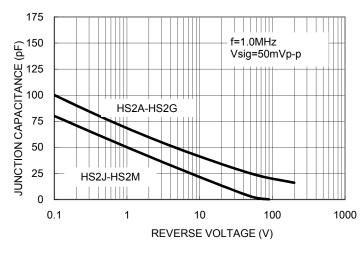
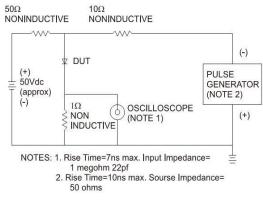


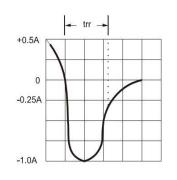


FIG. 4- TYPICAL JUNCTION CAPACITANCE

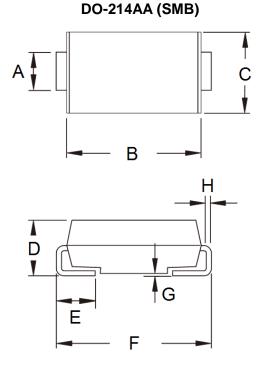
FIG.6- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM





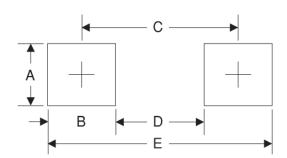


PACKAGE OUTLINE DIMENSIONS



DIM.	Unit	(mm)	Unit (inch)			
	Min	Max	Min	Мах		
А	1.95	2.10	0.077	0.083		
В	4.25	4.75	0.167	0.187		
С	3.48	3.73	0.137	0.147		
D	1.99	2.61	0.078	0.103		
E	0.90	1.41	0.035	0.056		
F	5.10	5.30	0.201	0.209		
G	0.10	0.20	0.004	0.008		
Н	0.15	0.31	0.006	0.012		

SUGGESTED PAD LAYOUT



P/N

YW

G

F

Symbol	Unit (mm)	Unit (inch)
A	2.3	0.091
В	2.5	0.098
С	4.3	0.169
D	1.8	0.071
E	6.8	0.268

MARKING DIAGRAM



- = Specific Device Code
- = Green Compound
- = Date Code
- = Factory Code



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HS2A R4GHS2JHR5HS2DHR5HS2MHR5HS2KHR5HS2B R5GHS2JHR4HS2FHR5HS2F R5HS2M R5GHS2BHR5HS2D R5GHS2AHR4HS2GHR4HS2F R5GHS2BHR4HS2A R5HS2G R5HS2G R5GHS2MHR4HS2K R5GHS2GHR5HS2DHR4HS2J R5GHS2KHR4HS2D R5HS2FHR4HS2A R5GHS2B R5HS2AHR5HS2K R5HS2D M4GHS2F M4GHS2G M4GHS2A M4GHS2B M4GHS2K M4GHS2M M4GHS2J M4G