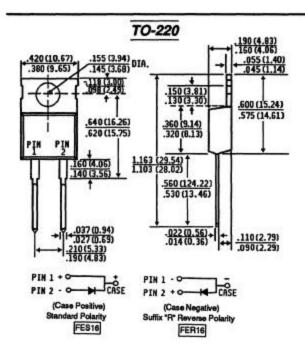
FES16AT THRU FES16JT

FEATURES



- Plastic package has Underwriters Laboratory Flammability Classification 94V-O
- · Glass passivated chip junctions
- Low power loss
- Low forward voltage, high current capability
- High surge capability
- Superfast recovery times, for high efficiency
- High temperature soldering guaranteed: 250°C, .25", (6.35mm) from case for 10 seconds



MECHANICAL DATA

Case: JEDEC TO-220 molded plastic Terminals: Plated Lead solderable per

MIL-STD-202, Method 208

Polarity: As marked Mounting Position: Any

Weight: 0.08 ounce, 2.24 gram

Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified. Resistive or inductive load.

For capacitive load, derate current by 20%.

	SYMBOLS	FES 16AT	FES 16BT	FES 16CT	FES 16DT	FES 16FT	FES 16GT	FES 16HT	FES 16JT	UNITS
Maximum Recurrent Peak Reverse Voltage	VRRM	50	100	150	200	300	400	500	600	Volts
Maximum RMS Voltage	VRMS	35	70	105	140	210	280	350	420	Volts
Maximum DC Blocking Voltage	VDC	50	100	150	200	300	400	500	600	Volts
Maximum Average Forward Rectified Current at Tc=100°C	I(AV)	16.0							Amps	
Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	IFSM	250.0							Amps	
Maximum Instantaneous Forward Voltage at 16A	VF	0.95 1.3 1.5				5	Volts			
Maximum DC Reverse Current Tc=25°C at Rated DC Blocking Voltage Tc=100°C	IR	10.0 500.0							μА	
Maximum Reverse Recovery Time (NOTE 2) T _J =25°C	TRR	35.0 50.0						nS		
Typical Junction Capacitance (NOTE 1)	CJ	175.0 145.0					pf			
Typical Thermal Resistance (NOTE 3)	ReJC	1.5						°C/W		
Operating and Storage Temperature Range	T _J ,T _{STG}	-65 to +150						°C		

NOTES

- Measured at 1 MHz and applied reverse voltage of 4.0 volts.
- Reverse Recovery Test Conditions: IF=0.5A, IR=1.0A, recover to 0.25A.
- 3. Thermal Resistance from Junction to Case.