2SK3525-01MR

FUJI POWER MOSFET

Super FAP-G Series

N-CHANNEL SILICON POWER MOSFET

■ Features

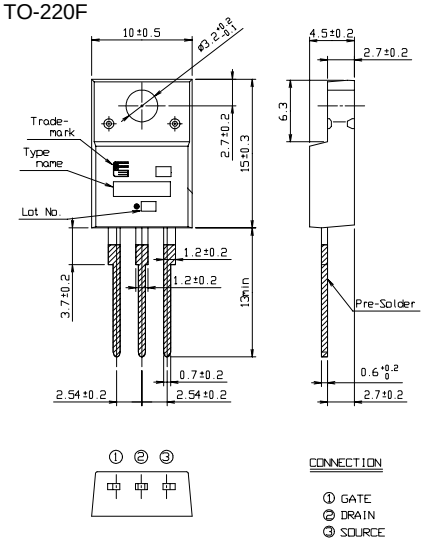
High speed switching
Low on-resistance
No secondary breadown
Low driving power
Avalanche-proof

■ Applications

Switching regulators
UPS (Uninterruptible Power Supply)
DC-DC converters

■ Outline Drawings

TO-220F



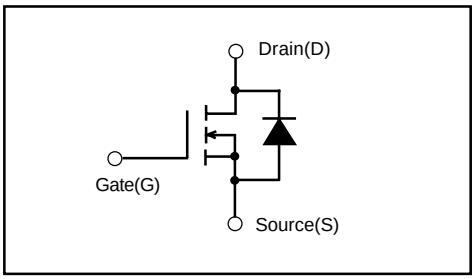
■ Maximum ratings and characteristicAbsolute maximum ratings

● **(Tc=25°C unless otherwise specified)**

Item	Symbol		Ratings	Unit
Drain-source voltage	V_{DS}		600	V
Continuous drain current	I_D		±6	A
Pulsed drain current	$I_{D(puls)}$		±24	A
Gate-source voltage	V_{GS}		±30	V
Repetitive or non-repetitive	I_{AR}	*2	6	A
Maximum Avalanche Energy	E_{AS}	*1	193	mJ
Maximum Drain-Source dV/dt	dV_{DS}/dt	*4	20	kV/µs
Peak Diode Recovery dV/dt	dV/dt	*3	5	kV/µs
Max. power dissipation	P_D	Ta=25°C	2.16	W
		Tc=25°C	58	
Operating and storage temperature range	T_{ch}		+150	°C
	T_{stg}		-55 to +150	°C

*1 L=9.83mH, Vcc=60V *2 Tch≦150°C *3 IF≦-ID, -di/dt=50A/µs, Vcc≦BVDSS, Tch≦150°C
*4 VDS≦600V

■ Equivalent circuit schematic



● **Electrical characteristics (T_C =25°C unless otherwise specified)**

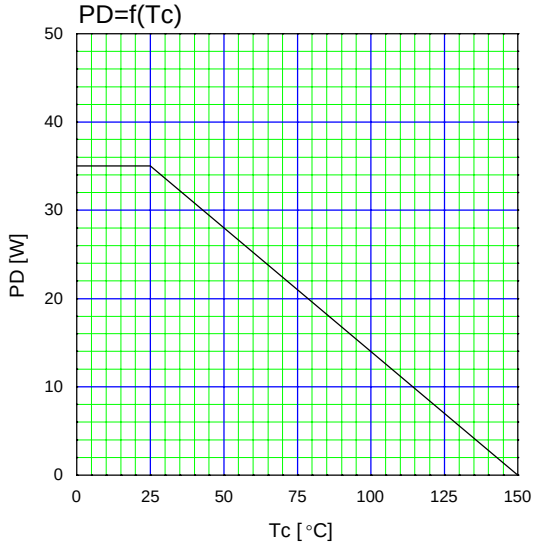
Item	Symbol	Test Conditions		Min.	Typ.	Max.	Units
Drain-source breakdown voltaget	$V_{(BR)DSS}$	I_D= 250µA V_{GS}=0V		600			V
Gate threshold voltage	$V_{GS(th)}$	I_D= 250µA V_{DS}=V_{GS}		3.0		5.0	V
Zero gate voltage drain current	I_{DSS}	V_{DS}=600V V_{GS}=0V	Tch=25°C			25	µA
		V_{DS}=480V V_{GS}=0V	Tch=125°C			250	
Gate-source leakage current	I_{GSS}	V_{GS}=±30V V_{DS}=0V			10	100	nA
Drain-source on-state resistance	$R_{DS(on)}$	I_D=3A V_{GS}=10V			0.93	1.20	Ω
Forward transcondutance	g_{fs}	I_D=3A V_{DS}=25V		3	6		S
Input capacitance	C_{iss}	V_{DS}=25V			750	1130	pF
Output capacitance	C_{oss}	V_{GS}=0V			100	150	
Reverse transfer capacitance	C_{rss}	f=1MHz			4.0	6.0	
Turn-on time t_{on}	$t_{d(on)}$	V_{CC}=300V I_D=3A			14	21	ns
	t_r	V_{GS}=10V			9	14	
Turn-off time t_{off}	$t_{d(off)}$	R_{GS}=10Ω			24	36	
	t_f				7	10.5	
Total Gate Charge	Q_G	V_{CC}=300V			20	30	nC
Gate-Source Charge	Q_{GS}	I_D=6A			8.5	13	
Gate-Drain Charge	Q_{GD}	V_{GS}=10V			5.5	8.5	
Avalanche capability	I_{AV}	L=9.83mH Tch=25°C		6			A
Diode forward on-voltage	V_{SD}	I_F=6A V_{GS}=0V Tch=25°C			1.00	1.50	V
Reverse recovery time	t_{rr}	I_F=6A V_{GS}=0V			0.7		µs
Reverse recovery charge	Q_{rr}	-di/dt=100A/µs Tch=25°C			3.5		µC

● **Thermalcharacteristics**

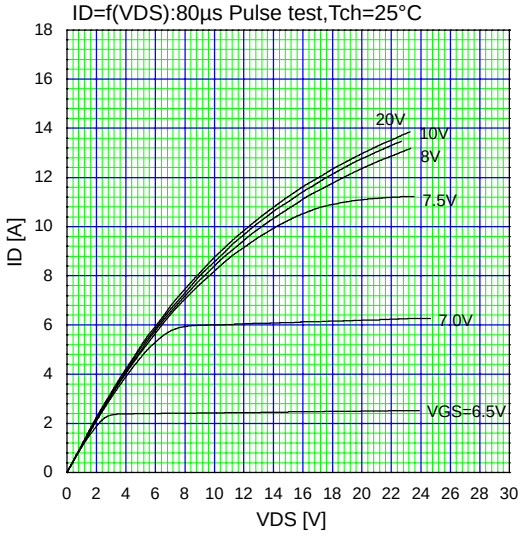
Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal resistance	$R_{th(ch-c)}$	channel to case			2.16	°C/W
	$R_{th(ch-a)}$	channel to ambient			35.0	°C/W

Characteristics

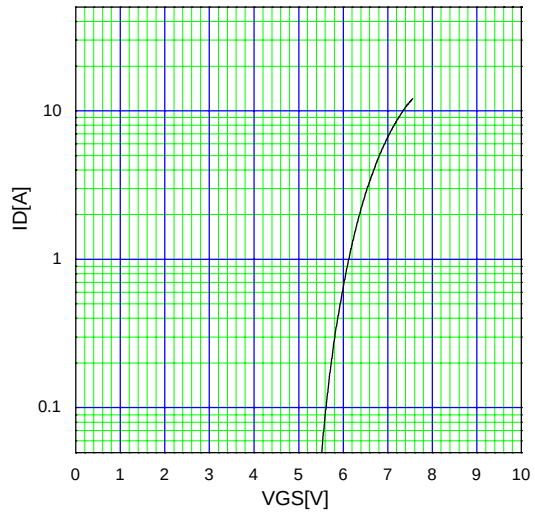
Allowable Power Dissipation
PD=f(Tc)



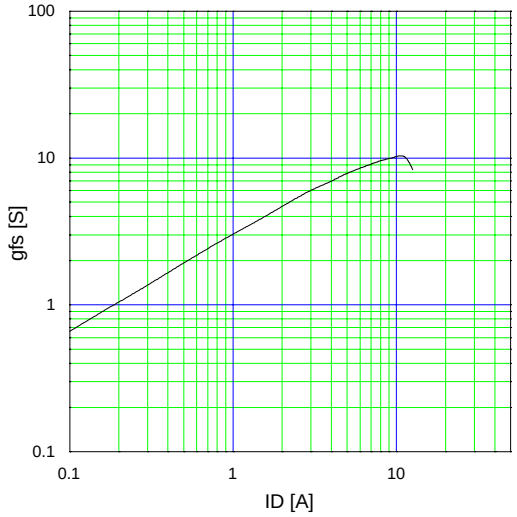
Typical Output Characteristics
ID=f(VDS):80µs Pulse test,Tch=25°C



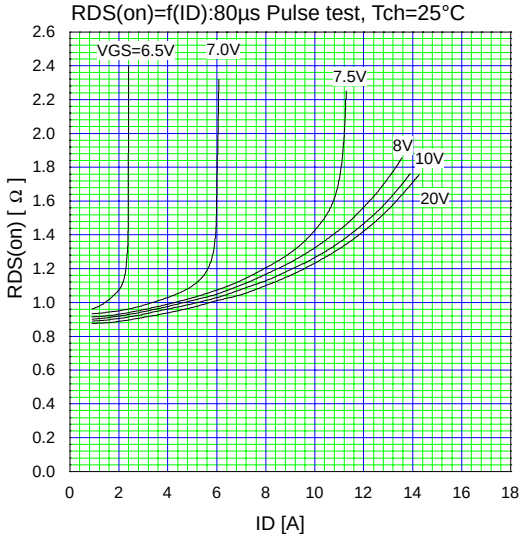
Typical Transfer Characteristic
ID=f(VGS):80µs Pulse test, VDS=25V,Tch=25°C



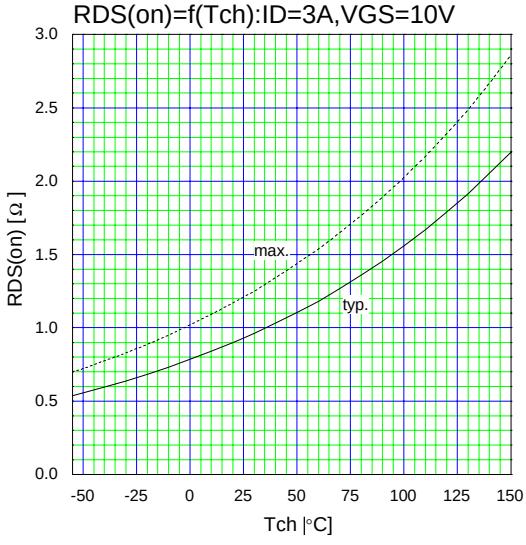
Typical Transconductance
gfs=f(ID):80µs Pulse test, VDS=25V ,Tch=25°C



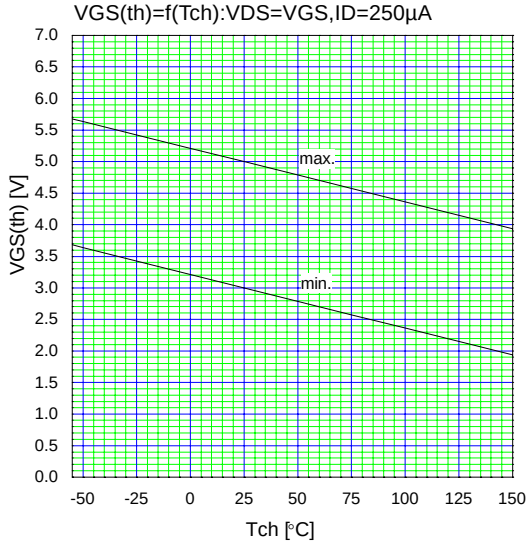
Typical Drain-Source on-state Resistance
RDS(on)=f(ID):80µs Pulse test, Tch=25°C



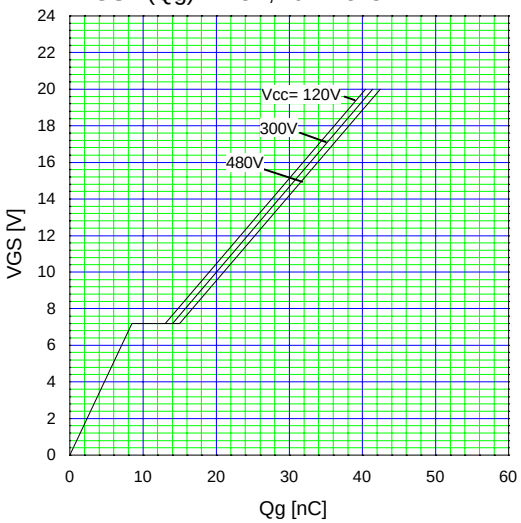
Drain-Source On-state Resistance
RDS(on)=f(Tch):ID=3A,VGS=10V



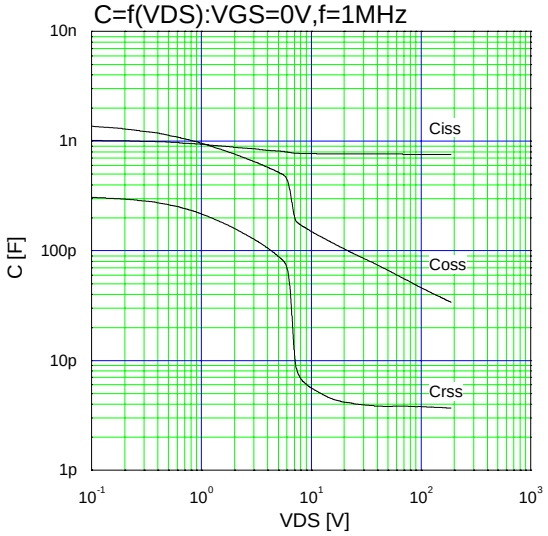
Gate Threshold Voltage vs. Tch
VGS(th)=f(Tch):VDS=VGS,ID=250μA



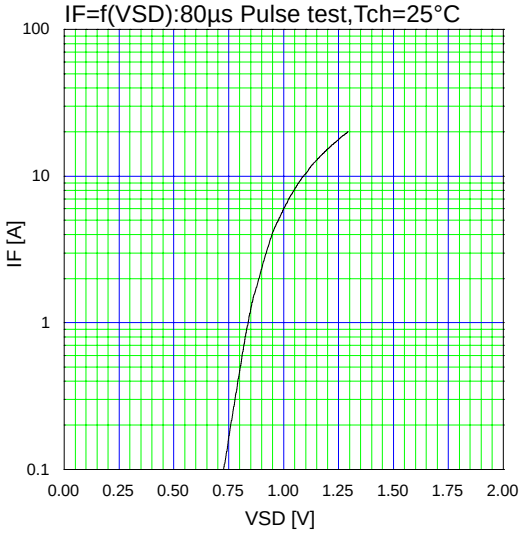
Typical Gate Charge Characteristics
VGS=f(Qg):ID=3A, Tch=25°C



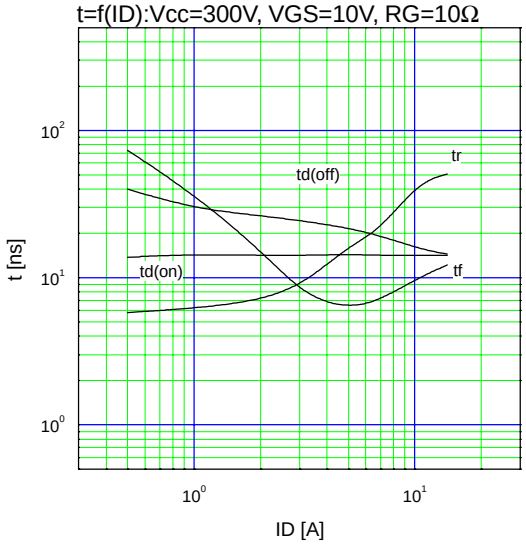
Typical Capacitance
C=f(VDS):VGS=0V,f=1MHz



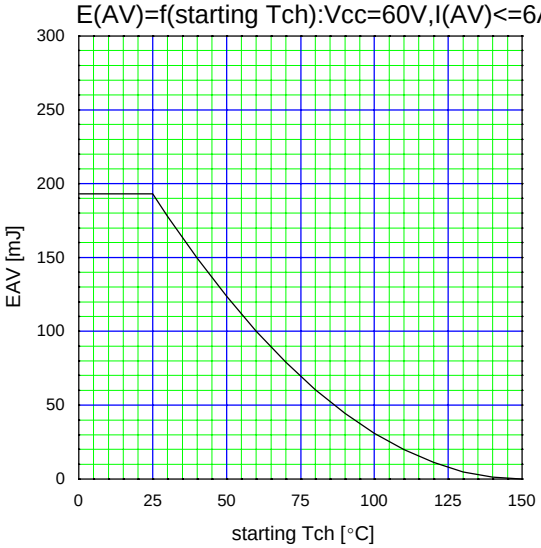
Typical Forward Characteristics of Reverse Diode
IF=f(VSD):80μs Pulse test,Tch=25°C



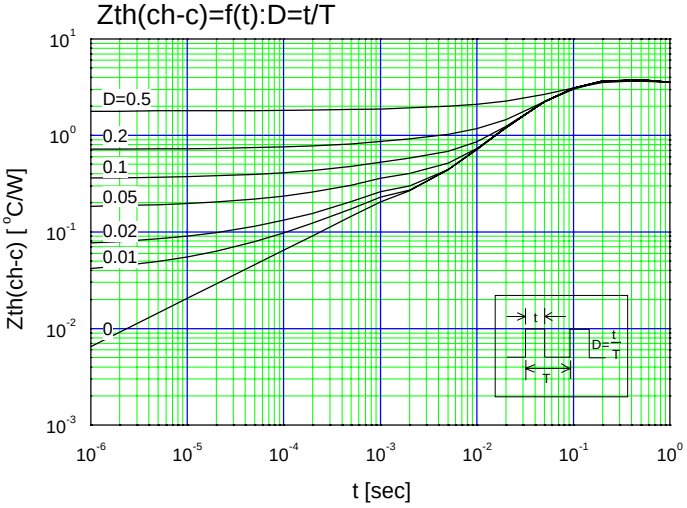
Typical Switching Characteristics vs. ID
t=f(ID):Vcc=300V, VGS=10V, RG=10Ω



Maximum Avalanche Energy vs. starting Tch
E(AV)=f(starting Tch):Vcc=60V,I(AV)<=6A



Transient Thermal Impedance
Zth(ch-c)=f(t):D=t/T



Maximum Avalanche Current Pulsewidth
I_{AV}=f(t_{AV}):starting Tch=25°C. Vcc=60V