

FGW85N60RB

Discrete IGBT

Reverse Blocking IGBT

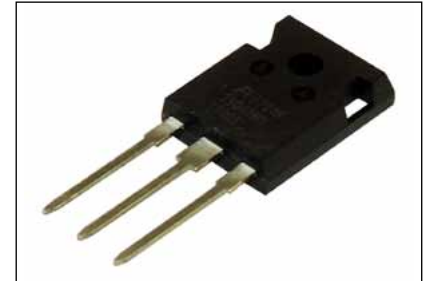
600V / 85A

■ Features

Reverse blocking characteristic for 1 chip by Fuji's original technology.
High efficiency by applying to T-type 3 level inverter circuit.

■ Applications

Uninterruptible power supply
Power conditioner
Battery system



■ Maximum Ratings and Characteristics

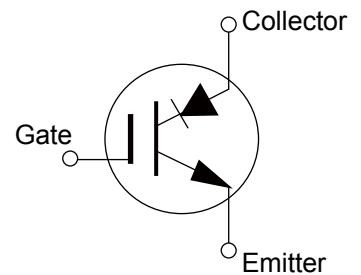
● Absolute Maximum Ratings at $T_j=25^\circ\text{C}$ (unless otherwise specified)

Items	Symbols	Characteristics	Units	Remarks
Collector-Emitter Voltage	V_{CES}	600	V	
Repetitive Peak Reverse Voltage	V_{RRM}	600	V	
Gate-Emitter Voltage	V_{GES}	± 20	V	
DC Collector Current	$I_{C@25}$	100	A	$T_c=25^\circ\text{C}, T_j=150^\circ\text{C}$ Note *1
	$I_{C@100}$	85	A	$T_c=100^\circ\text{C}, T_j=150^\circ\text{C}$
Pulsed Collector Current	I_{CP}	170	A	Note *2
Turn-Off Safe Operating Area	-	170	A	$V_{CE} \leq 600\text{V}, T_j \leq 150^\circ\text{C}$
Short Circuit Withstand Time	t_{SC}	10	μs	$V_{CC} \leq 300\text{V}, V_{GE} = 15\text{V}$ $T_j \leq 150^\circ\text{C}$
IGBT Max. Power Dissipation	P_{D_IGBT}	600	W	$T_c=25^\circ\text{C}$
Operating Junction Temperature	T_j	-40 ~ +150	$^\circ\text{C}$	
Storage Temperature	T_{stg}	-55 ~ +150	$^\circ\text{C}$	

Note *1 : Current value limited by bonding wire.

Note *2 : Pulse width limited by T_{jmax} .

■ Equivalent circuit



● Electrical characteristics at $T_j=25^\circ\text{C}$ (unless otherwise specified)

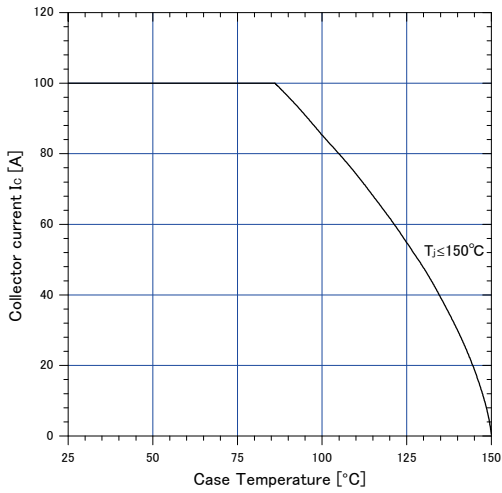
Description	Symbols	Conditions	Characteristics			Units	
			min.	typ.	max.		
Collector-Emitter Breakdown Voltage	$V_{(BR)CES}$	$I_c = 1\text{mA}, V_{GE} = 0\text{V}$	600	-	-	V	
Zero Gate Voltage Collector Current	I_{CES}	$V_{CE} = 600\text{V}, V_{GE} = 0\text{V}$	-	-	250	μA	
		$T_j=150^\circ\text{C}$	-	-	10	mA	
Gate-Emitter Leakage Current	I_{GES}	$V_{CE} = 0\text{V}, V_{GE} = \pm 20\text{V}$	-	-	200	nA	
Gate-Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = +20\text{V}, I_c = 85\text{mA}$	5.5	6.5	7.5	V	
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$V_{GE} = +15\text{V}, I_c = 85\text{A}$	-	2.45	2.80	V	
		$T_j=150^\circ\text{C}$	-	2.95	-		
Input Capacitance	C_{ies}	$V_{CE}=10\text{V}$	-	5100	-	pF	
Output Capacitance	C_{oes}	$V_{GE}=0\text{V}$	-	1150	-		
Reverse Transfer Capacitance	C_{res}	$f=1\text{MHz}$	-	740	-		
Gate Charge	Q_G	$V_{CC} = 400\text{V}$ $I_c = 85\text{A}$ $V_{GE} = 15\text{V}$	-	300	-	nC	
Turn-On Delay Time	$t_{d(on)}$	$T_j = 25^\circ\text{C}$	-	35	-	ns	
Rise Time	t_r	$V_{CC} = 400\text{V}$	-	85	-		
Turn-Off Delay Time	$t_{d(off)}$	$I_c = 85\text{A}$	-	175	-		
Fall Time	t_f	$V_{GE} = \pm 15\text{V}$	-	64	-		
Turn-On Energy	E_{on}	$R_G = 10\Omega$ $L = 500\mu\text{H}$	-	4.7	-	mJ	
Turn-Off Energy	E_{off}	Energy loss include "tail" and FWD (FDRW30S120J) reverse recovery.	-	2.4	-		
Turn-On Delay Time	$t_{d(on)}$	$T_j = 150^\circ\text{C}$	-	30	-	ns	
Rise Time	t_r	$V_{CC} = 400\text{V}$	-	125	-		
Turn-Off Delay Time	$t_{d(off)}$	$I_c = 85\text{A}$	-	185	-		
Fall Time	t_f	$V_{GE} = \pm 15\text{V}$	-	66	-		
Turn-On Energy	E_{on}	$R_G = 10\Omega$ $L = 500\mu\text{H}$	-	6.0	-	mJ	
Turn-Off Energy	E_{off}	Energy loss include "tail" and FWD (FDRW30S120J) reverse recovery.	-	3.0	-		
Reverse Recovery Time	t_{rr}	$V_{CC} = 400\text{V}$ $I_c = 85\text{A}$ $V_{GE} = \pm 15\text{V}$ $R_G = 30\Omega$ $L = 500\mu\text{H}$	$T_j=25^\circ\text{C}$	-	165	-	ns
		$T_j=150^\circ\text{C}$	-	330	-		

● Thermal Resistance

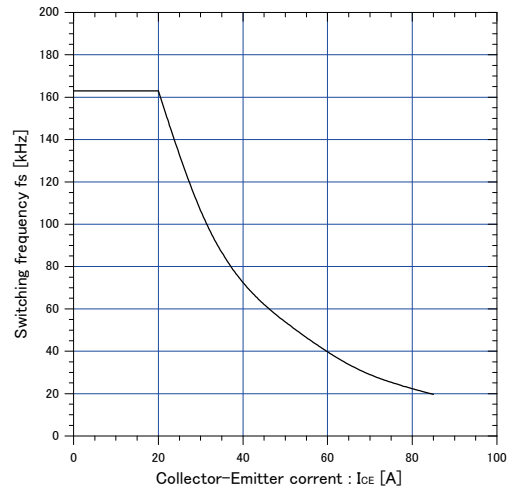
Description	Symbols	min.	typ.	max.	Units
Thermal Resistance, Junction-Ambient	$R_{th(j-a)}$	-	-	50	°C/W
Thermal Resistance, Junction to Case	$R_{th(j-c)}$	-	-	0.208	

■ Characteristics (Representative)

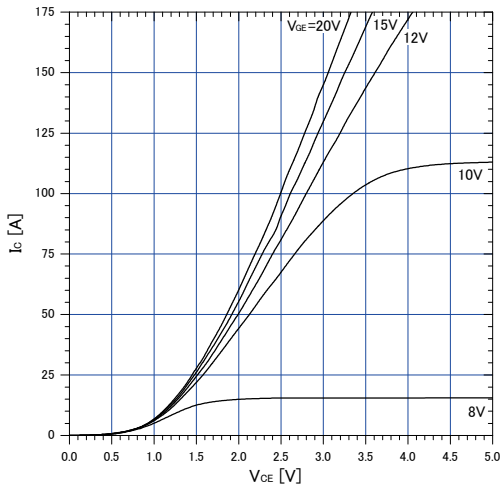
Graph.1
DC Collector Current vs T_c
 $V_{GE} \geq +15V, T_c \leq 150^\circ C$



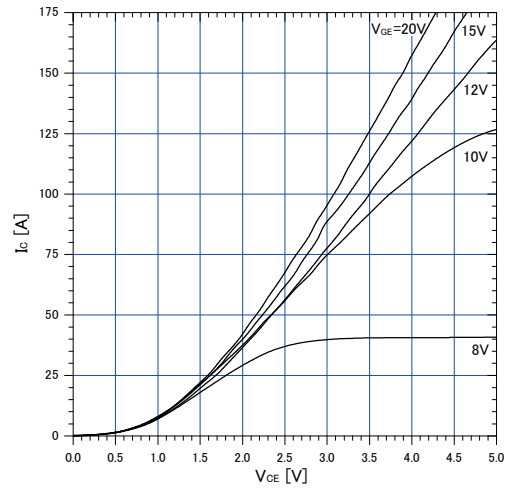
Graph.2
Collector Current vs. switching frequency
 $V_{GE} = +15V/-15V, T_c \leq 150^\circ C, V_{CC} = 400V,$
 $D = 0.5, R_G = 10\Omega, T_c = 100^\circ C$



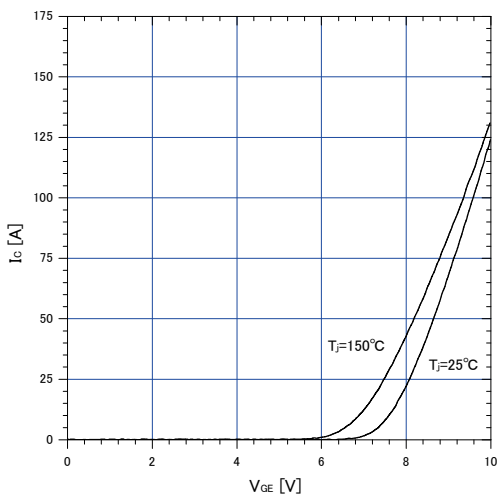
Graph.3
Typical Output Characteristics ($V_{CE}-I_c$)
 $T_j = 25^\circ C$



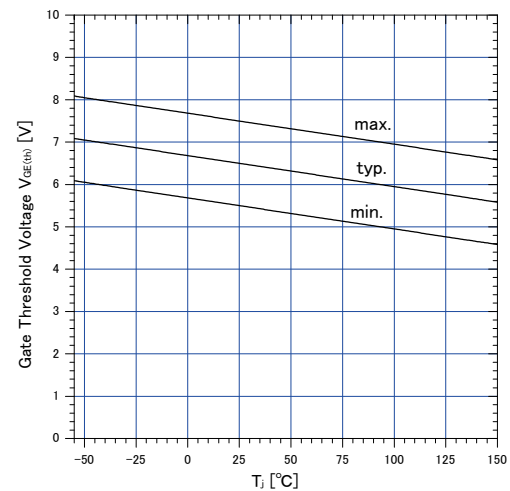
Graph.4
Typical Output Characteristics ($V_{CE}-I_c$)
 $T_j = 150^\circ C$



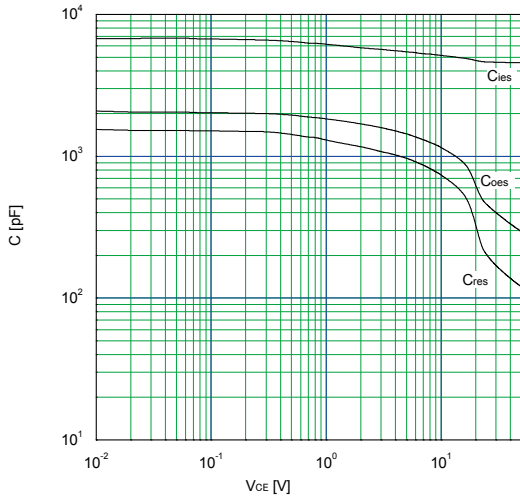
Graph.5
Typical Transfer Characteristics
 $V_{GE} = +15V$



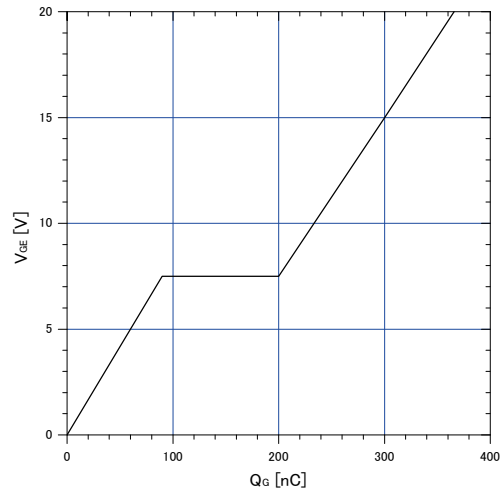
Graph.6
Gate Threshold Voltage vs. T_j
 $I_c = 85mA, V_{CE} = 20V$



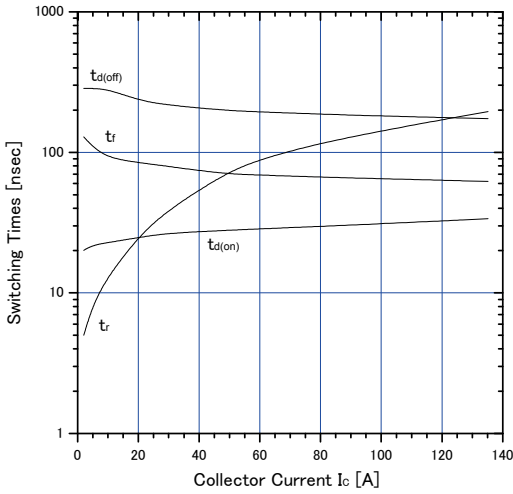
Graph.7
Typical Capacitance
 $V_{GE}=0V, f=1MHz, T_j=25^\circ C$



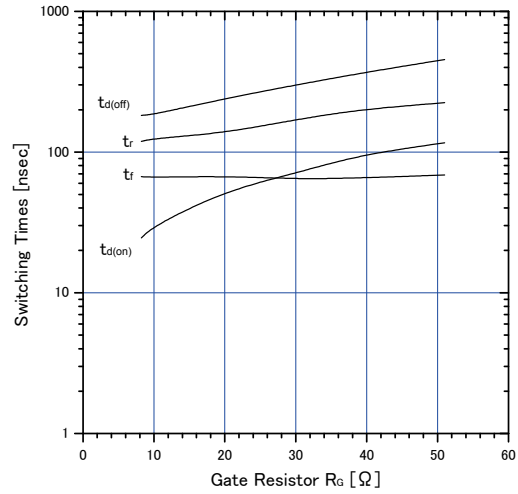
Graph.8
Typical Gate Charge
 $V_{CC}=400V, I_c=85A, T_j=25^\circ C$



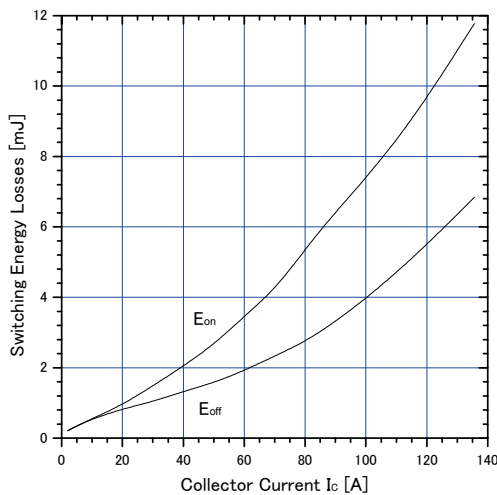
Graph.9
Typical switching time vs. I_c
 $T_j=150^\circ C, V_{CC}=400V, L=500\mu H$
 $V_{GE}=+15V/-15V, R_G=10\Omega$



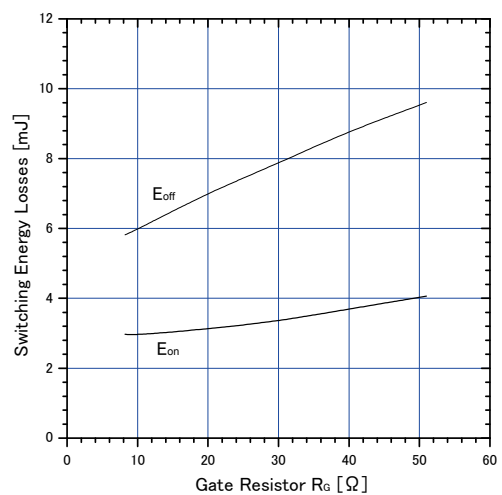
Graph.10
Typical switching time vs. R_G
 $T_j=150^\circ C, V_{CC}=400V, I_c=85A, L=500\mu H$
 $V_{GE}=+15V/-15V$



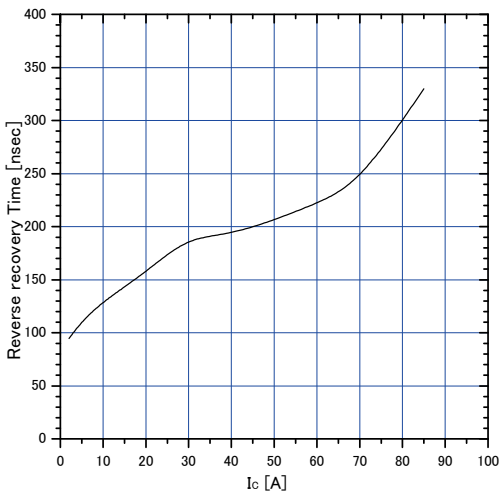
Graph.11
Typical switching losses vs. I_c
 $T_j=150^\circ C, V_{CC}=400V, L=500\mu H$
 $V_{GE}=+15V/-15V, R_G=10\Omega$



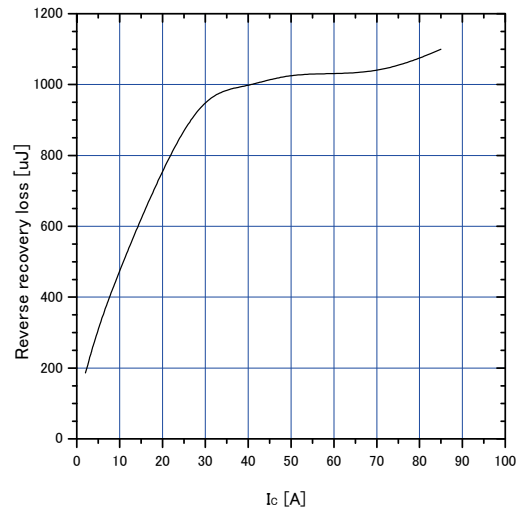
Graph.12
Typical switching losses vs. R_G
 $T_j=150^\circ C, V_{CC}=400V, I_c=85A, L=500\mu H$
 $V_{GE}=+15V/-15V$



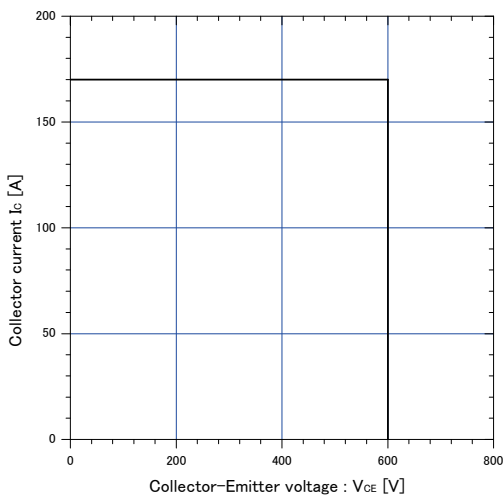
Graph.13
 Typical reverse recovery time vs. I_c
 $T_j=150^\circ\text{C}, V_{cc}=400\text{V}, L=500\mu\text{H}$
 $V_{GE}=+15/-15\text{V}, R_G=30\Omega$



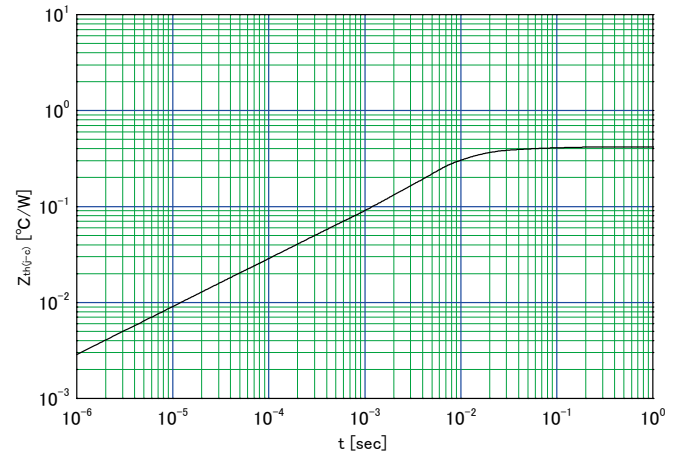
Graph.14
 Typical reverse recovery loss vs. I_c
 $T_j=150^\circ\text{C}, V_{cc}=400\text{V}, L=500\mu\text{H}$
 $V_{GE}=+15/-15\text{V}, R_G=30\Omega$



Graph.15
 Reverse biased Safe Operating Area
 $T_j \leq 150^\circ\text{C}, V_{GE}=+15\text{V}/0\text{V}, R_G=10\Omega$

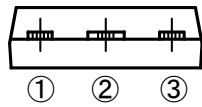
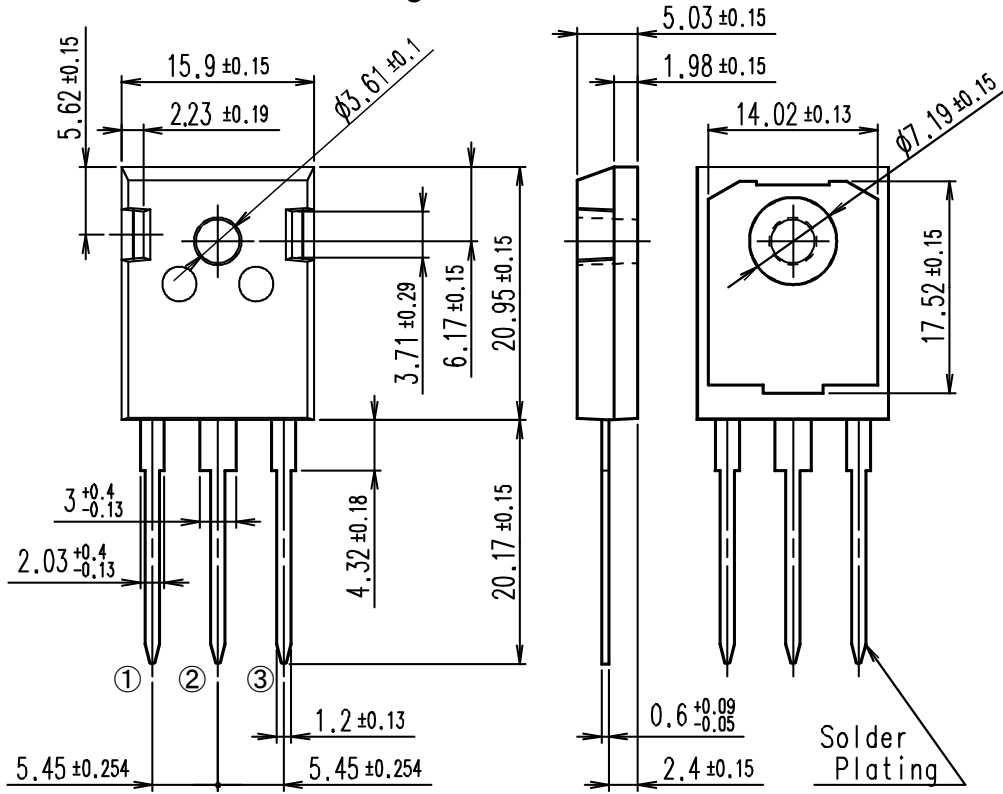


Graph.16
 Transient thermal resistance



■ Outline Drawings, mm

Outview : TO-247 Package



CONNECTION

- ① GATE
- ② COLLECTOR
- ③ EMITTER

DIMENSIONS ARE IN MILLIMETERS.

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